

Fig. 1

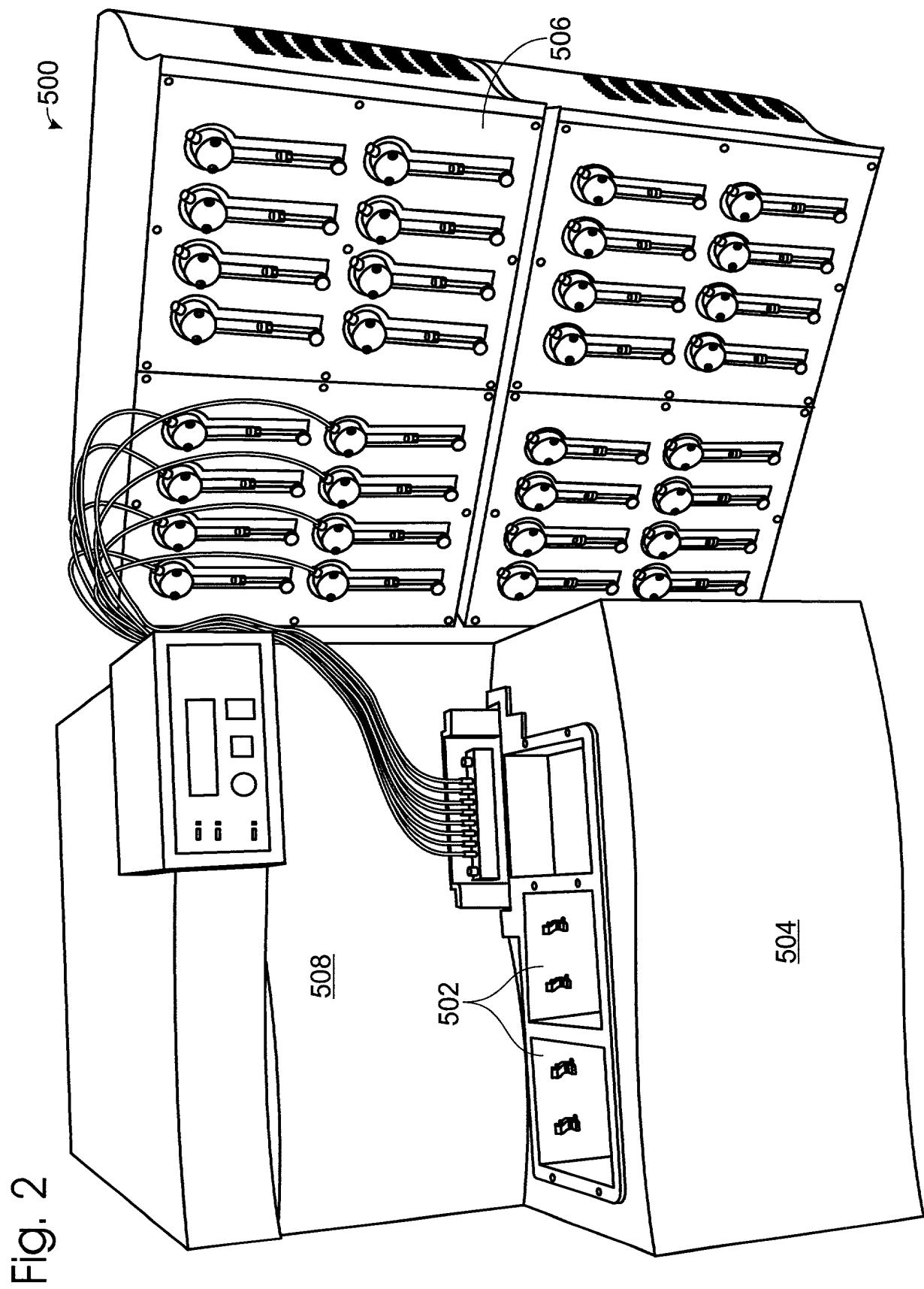


Fig. 3

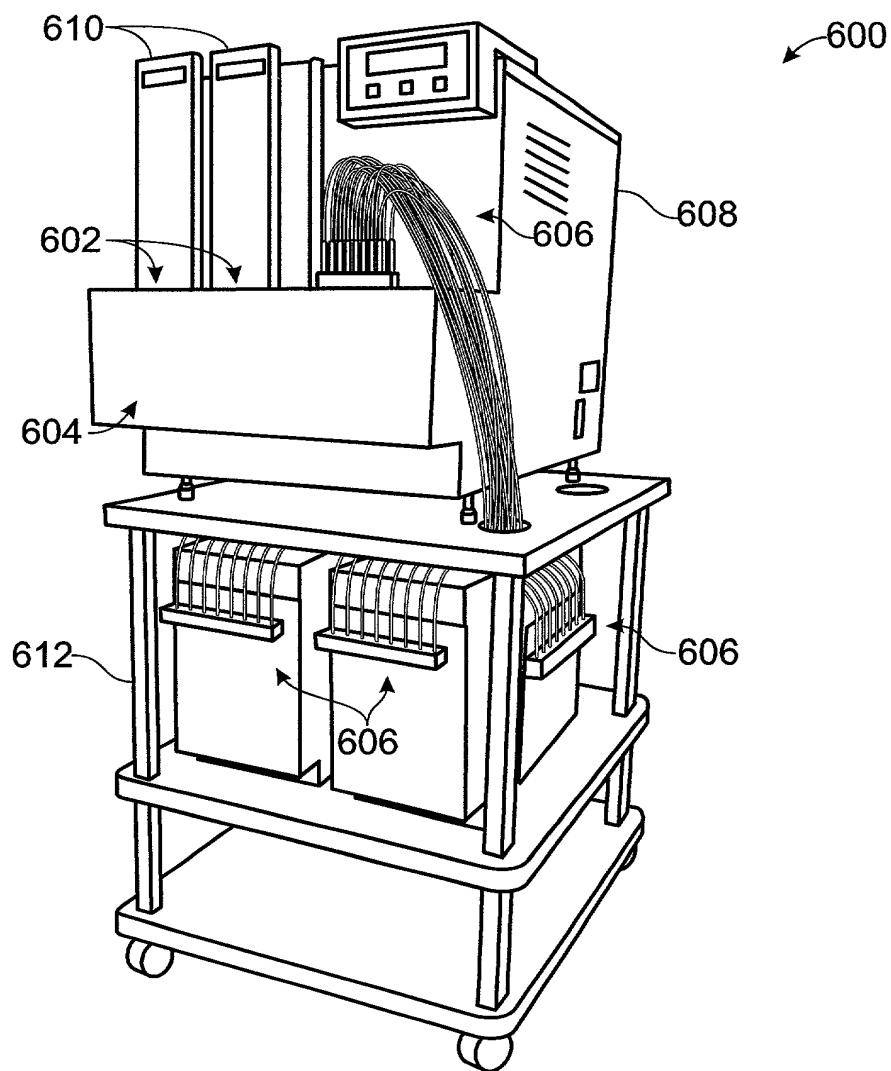
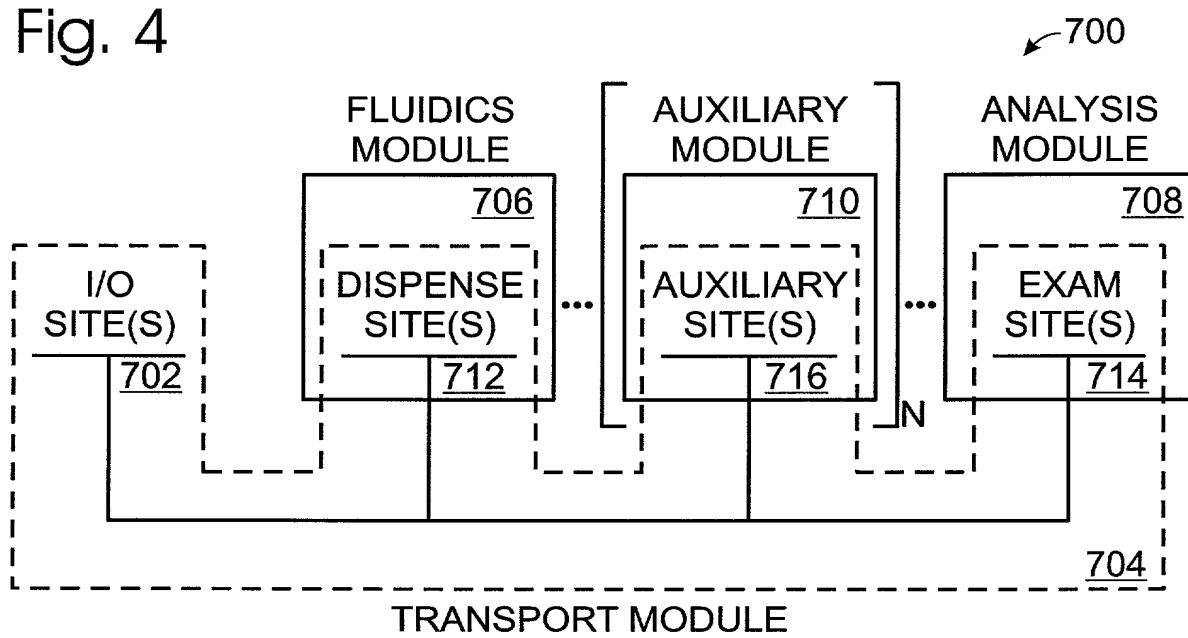
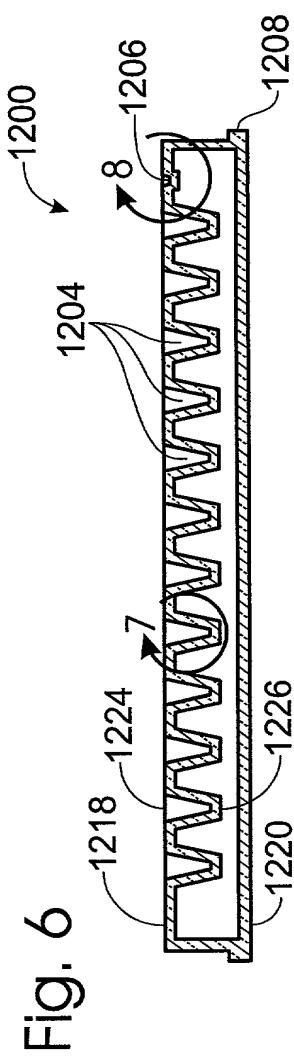
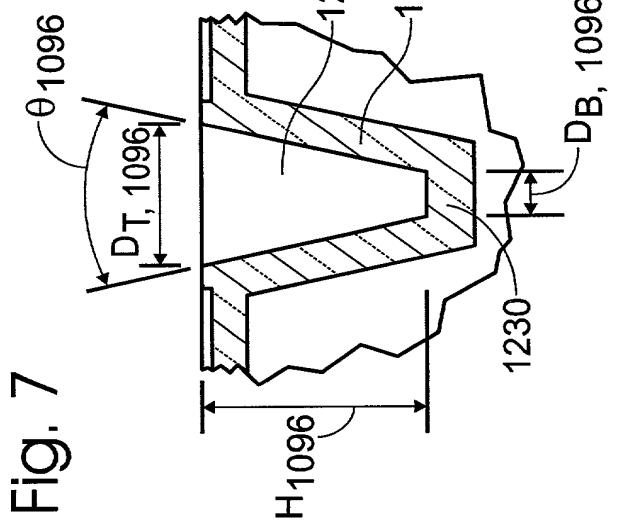
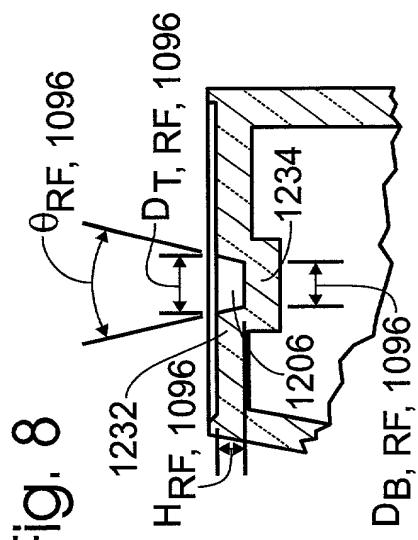
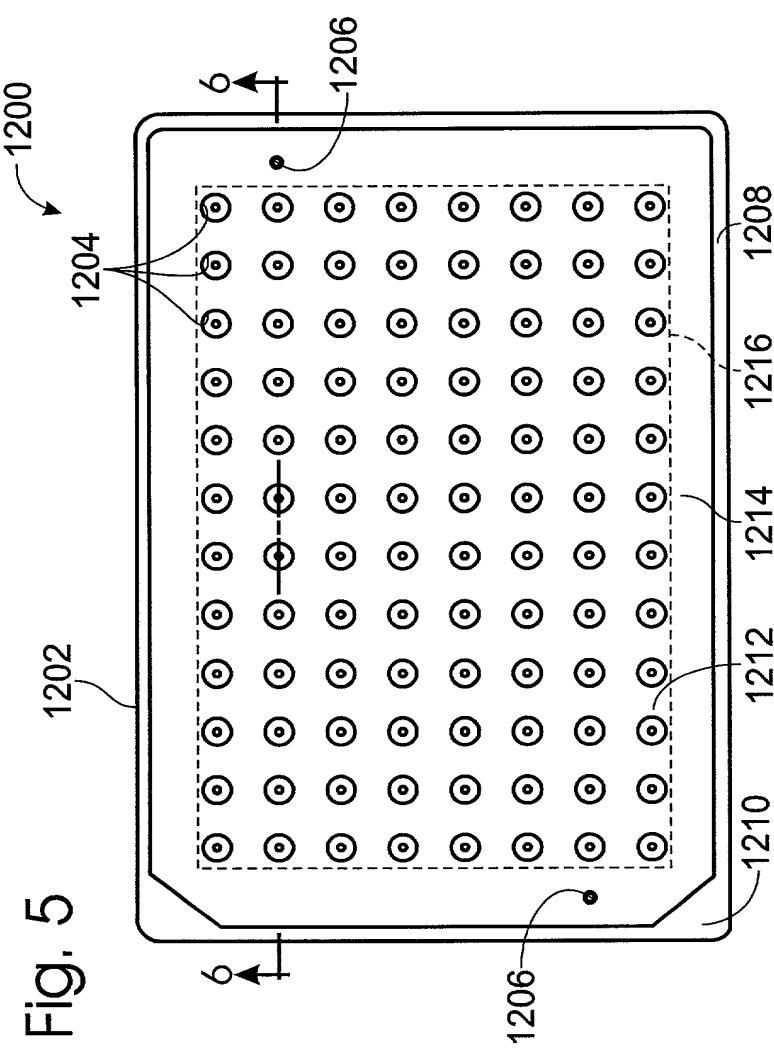


Fig. 4





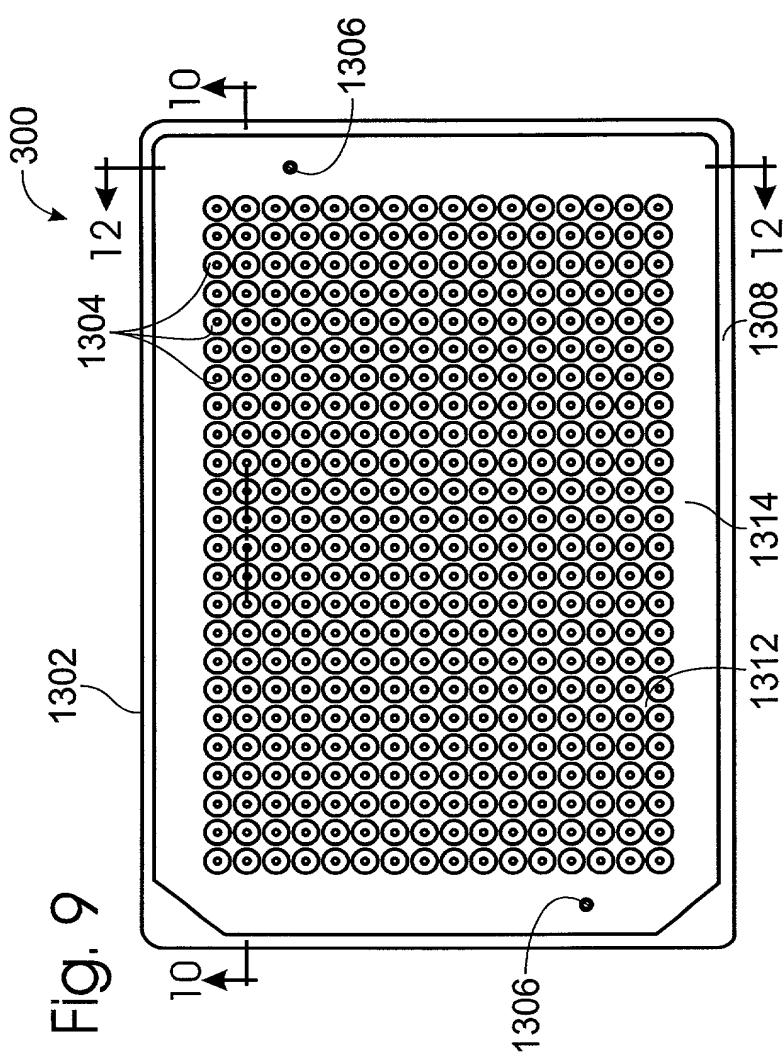
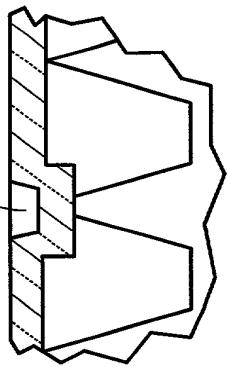
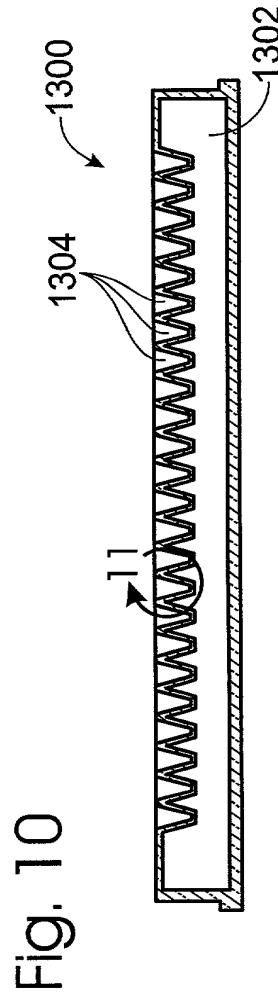
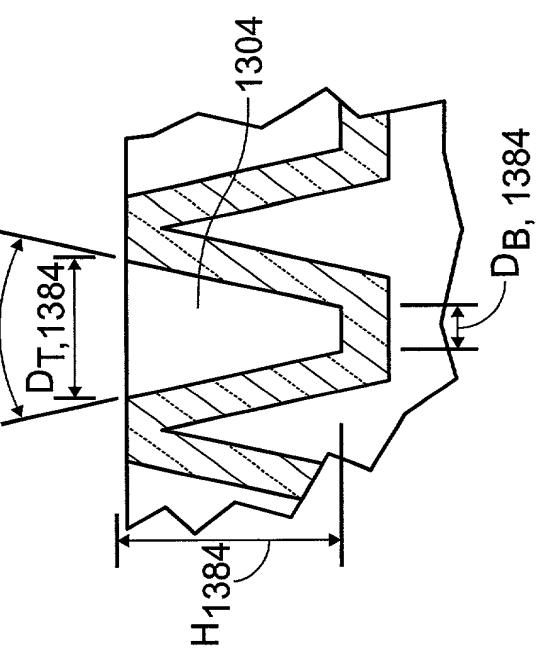


Fig. 12



1306

Fig. 11



DB, 1384

Fig. 13

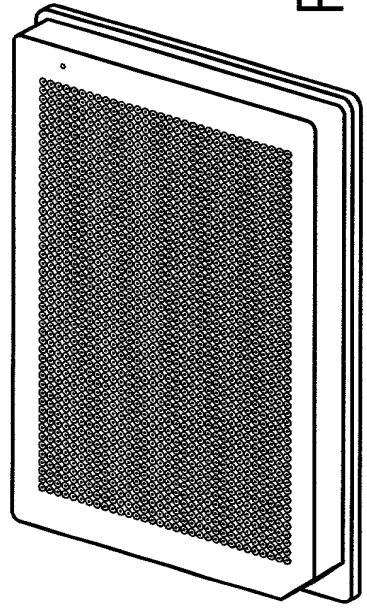


Fig. 14

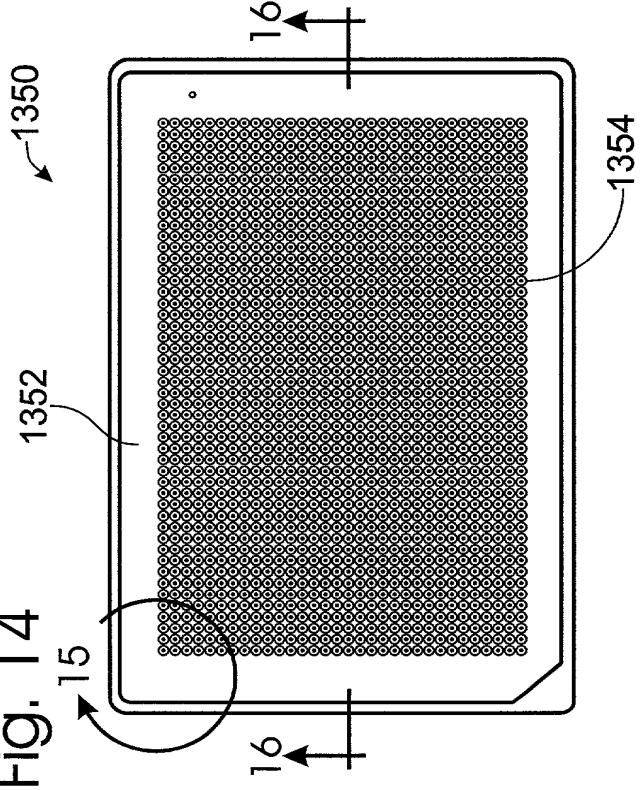


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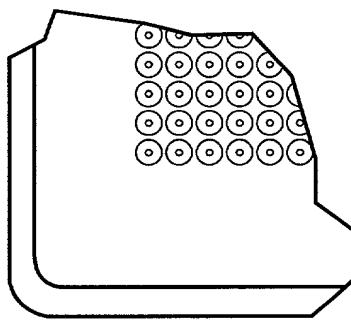


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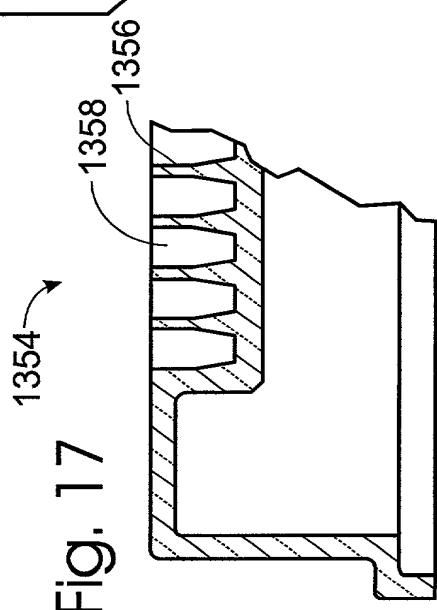
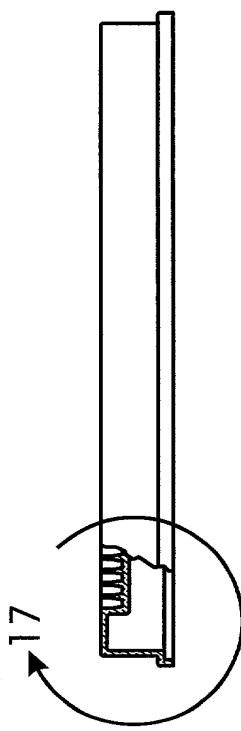


Fig. 16



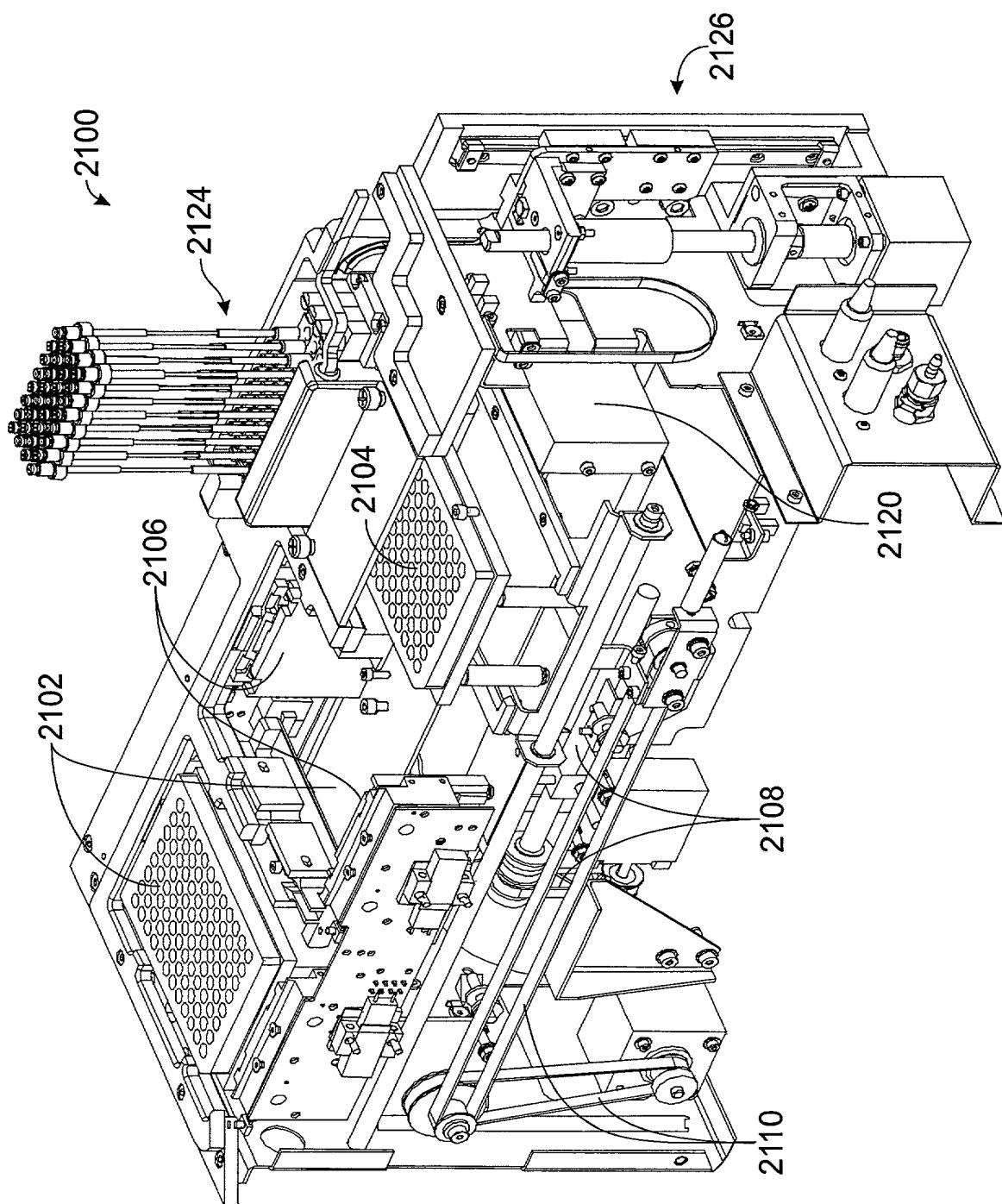


Fig. 18

Fig. 19

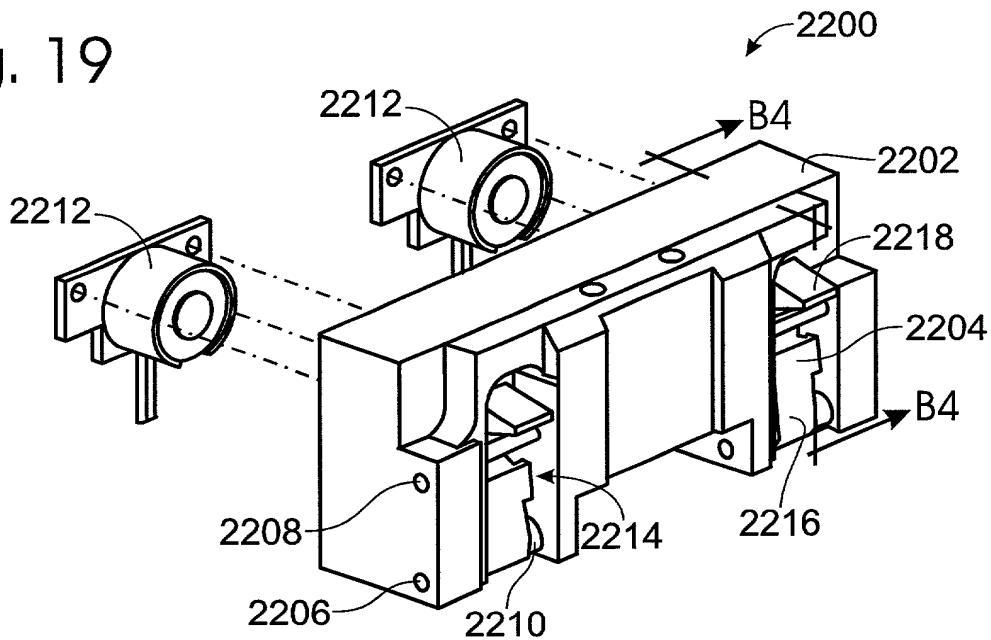


Fig. 20

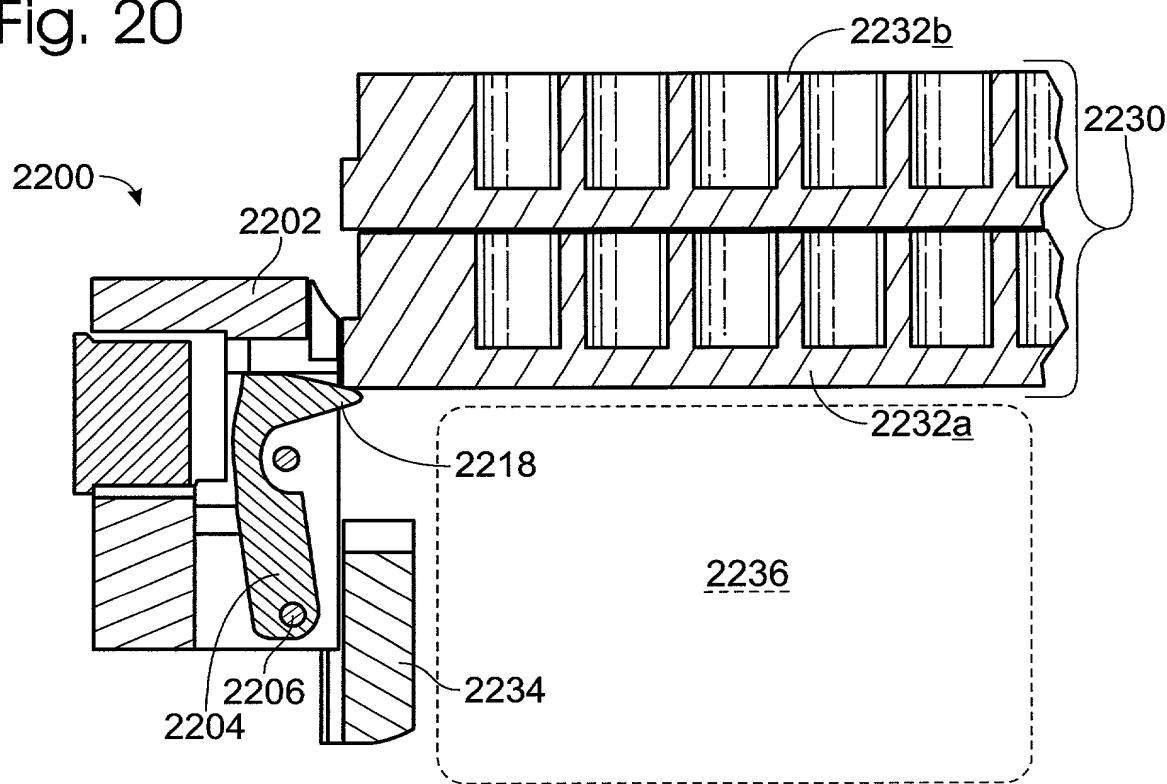


Fig. 21 (INPUT CYCLE)

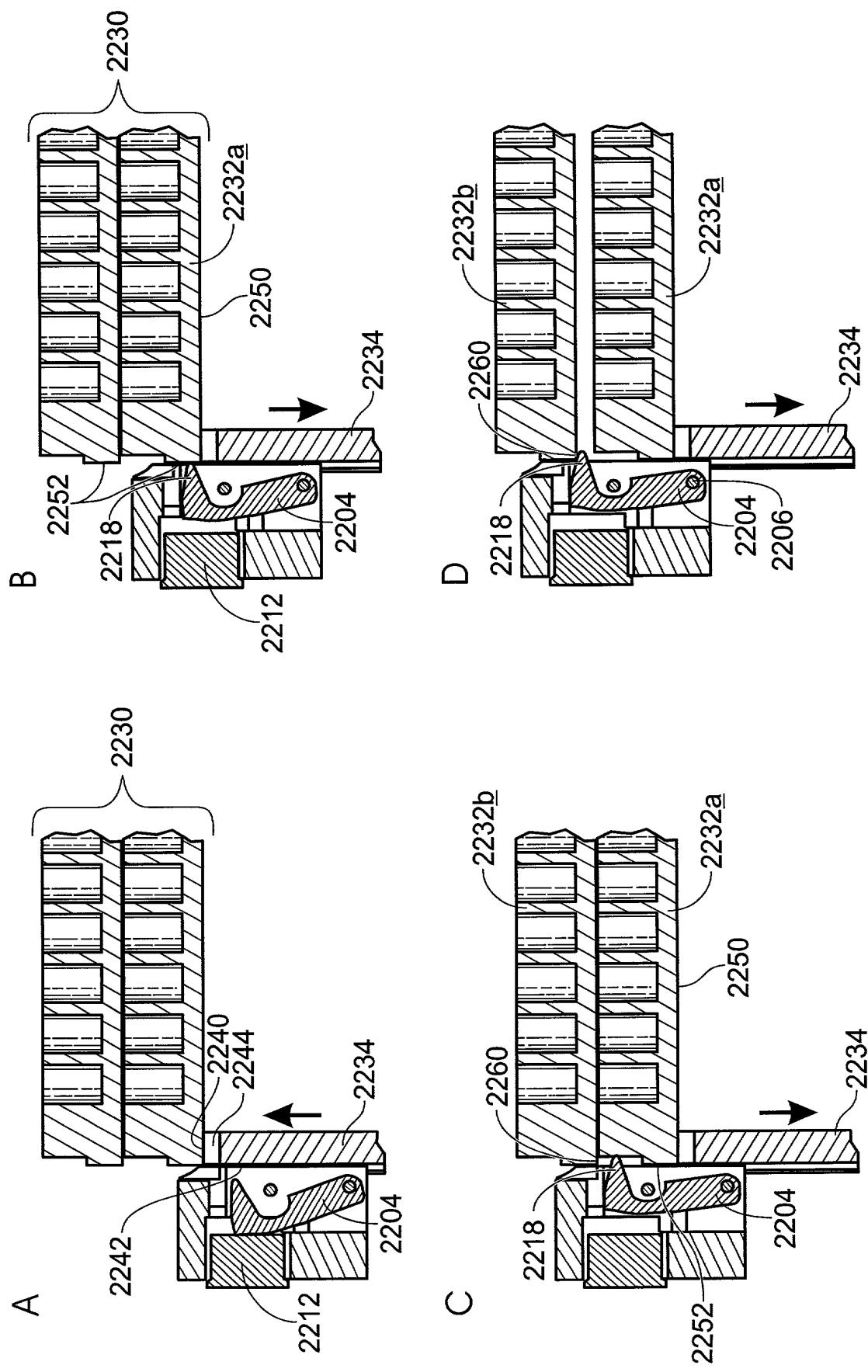


Fig. 22 (OUTPUT CYCLE)

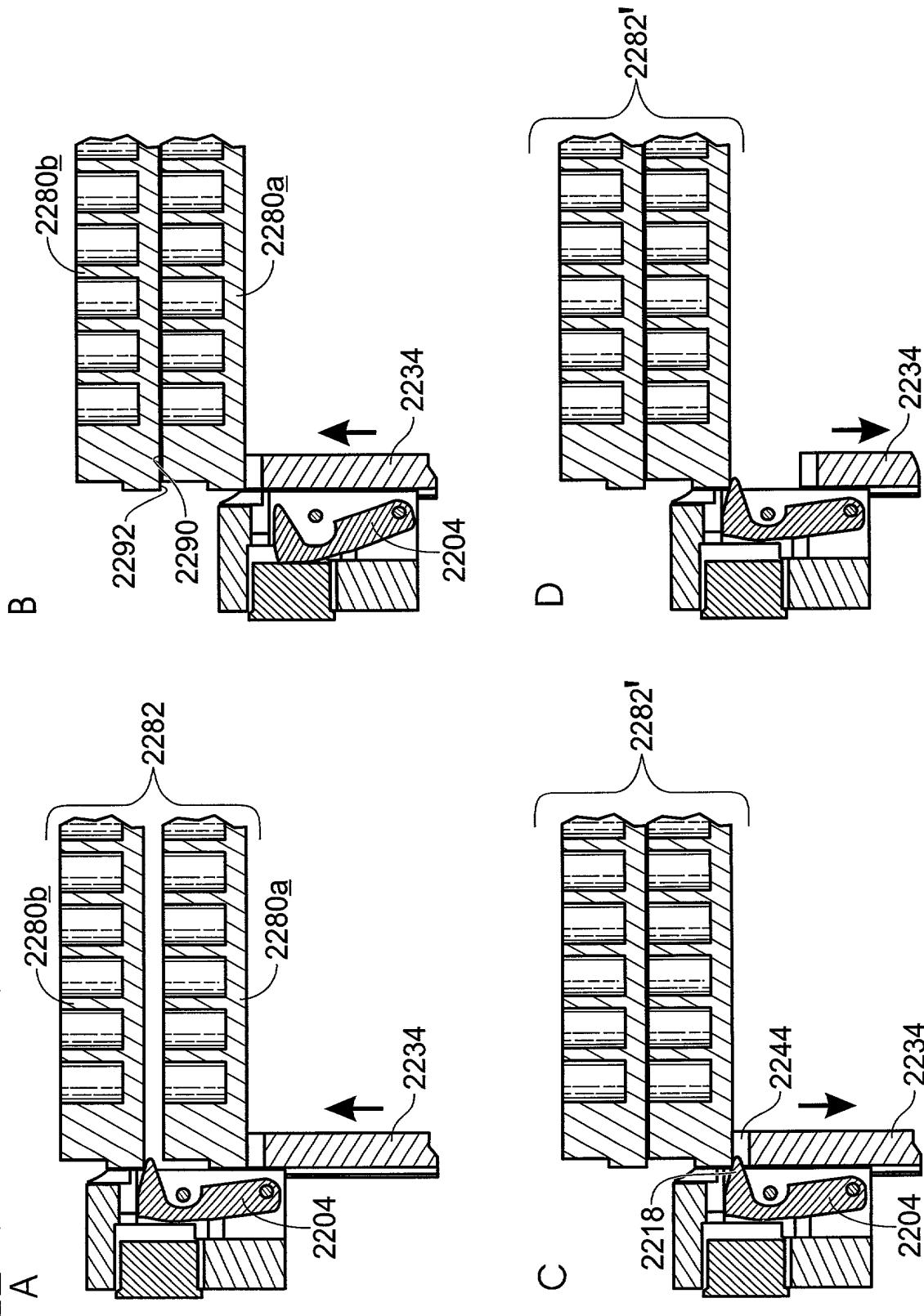


Fig. 23

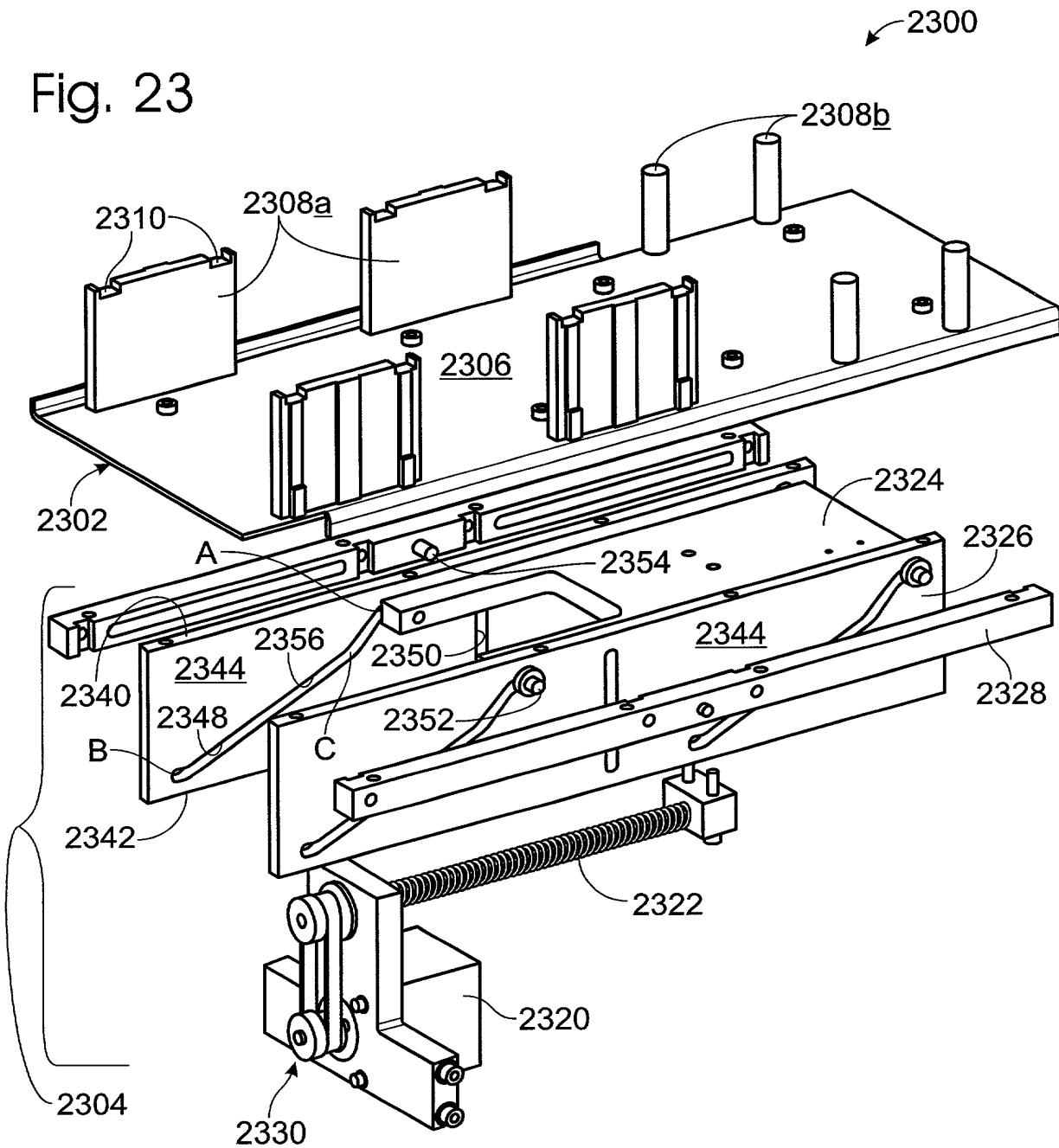


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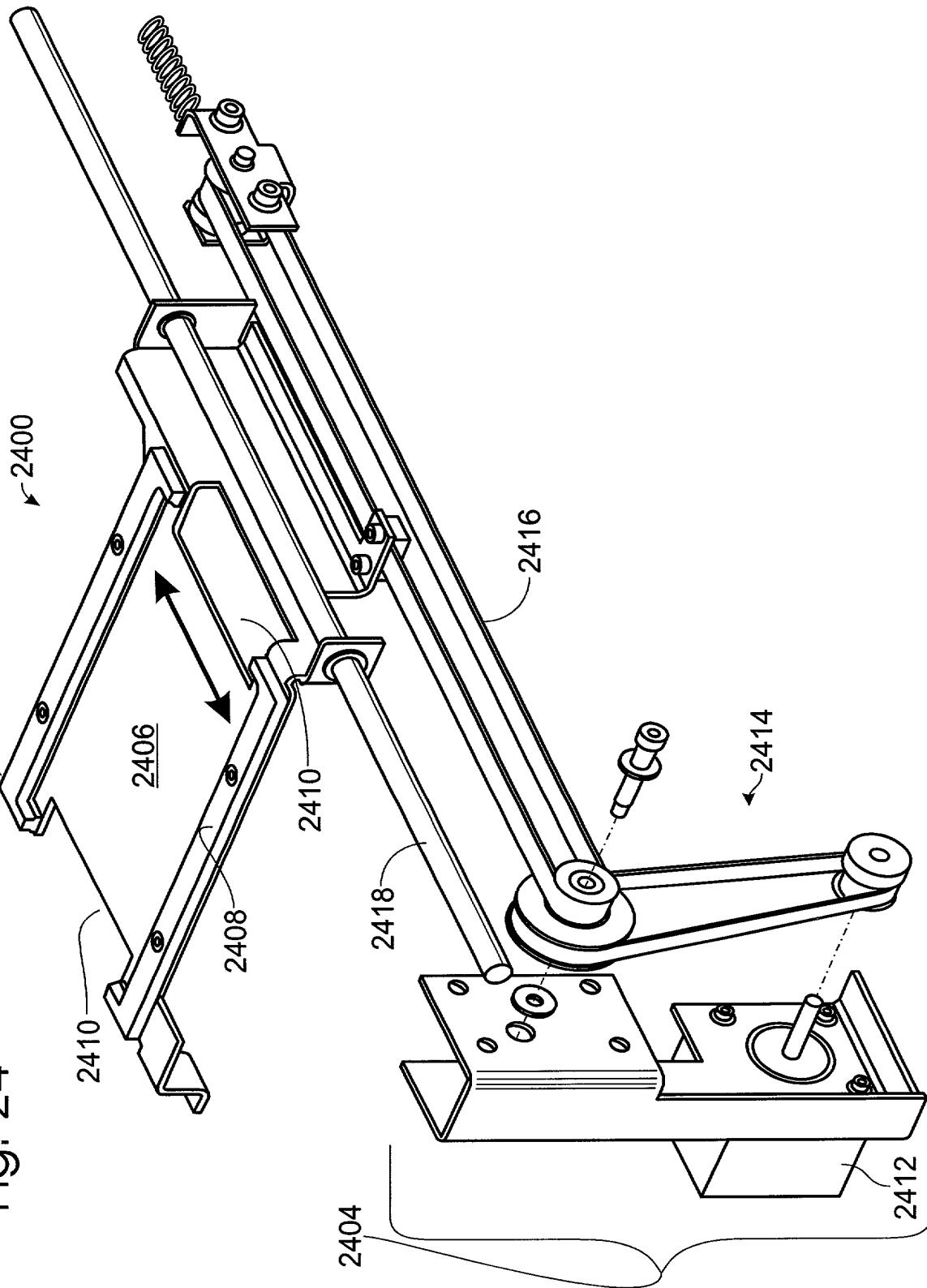


Fig. 25

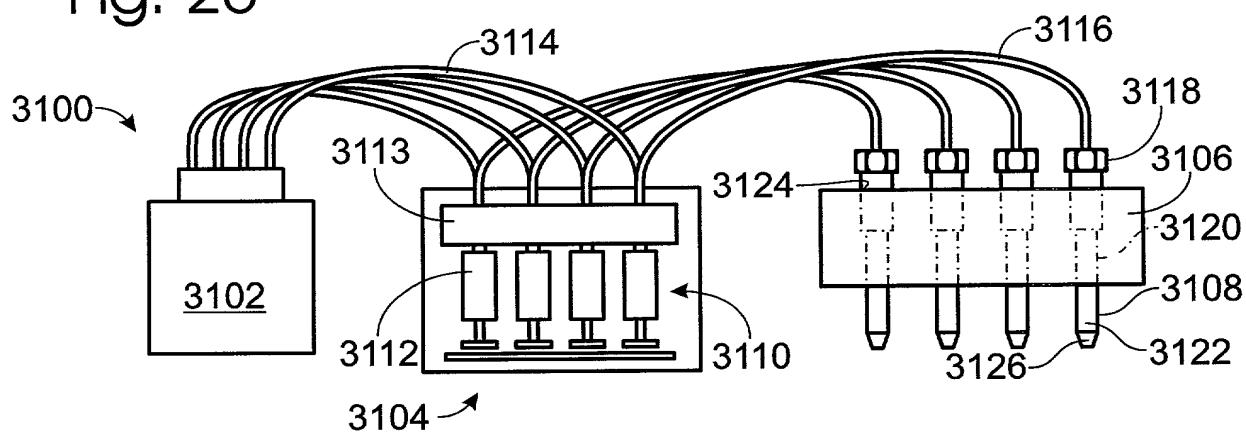


Fig. 26

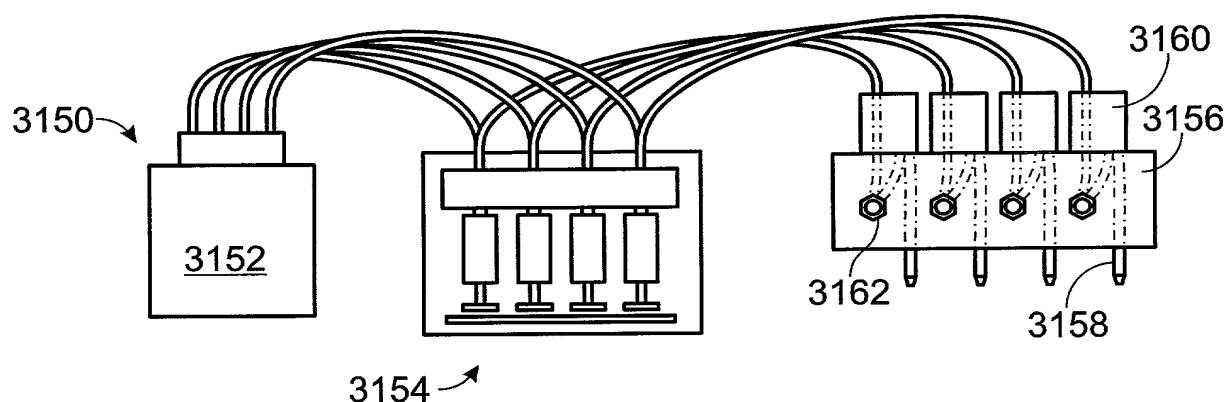


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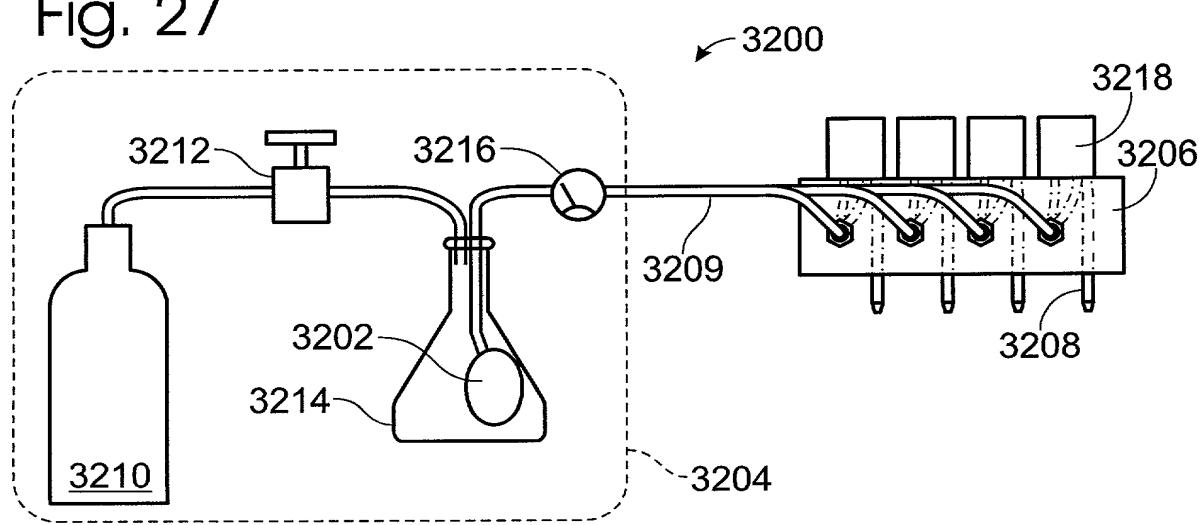


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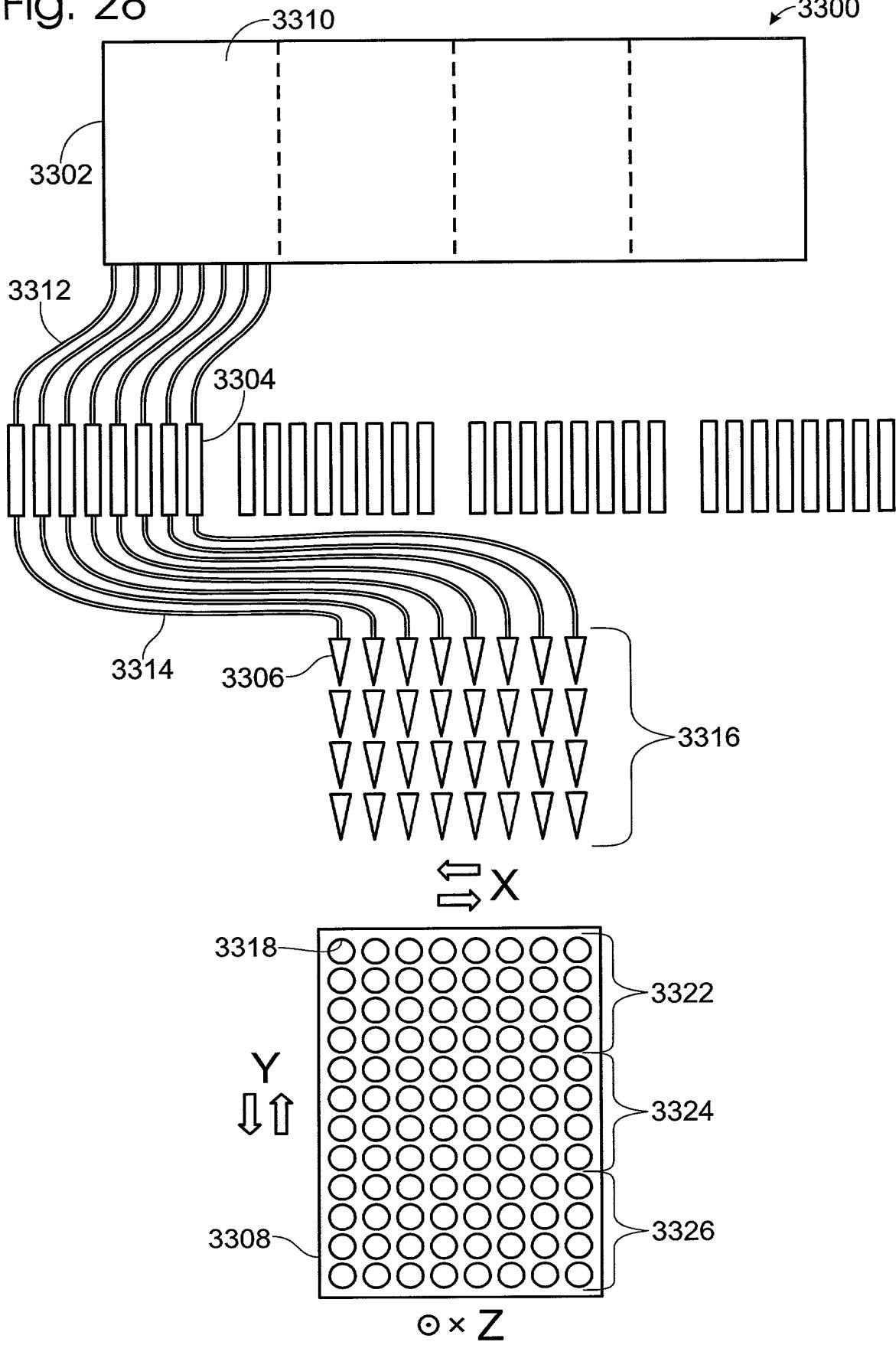


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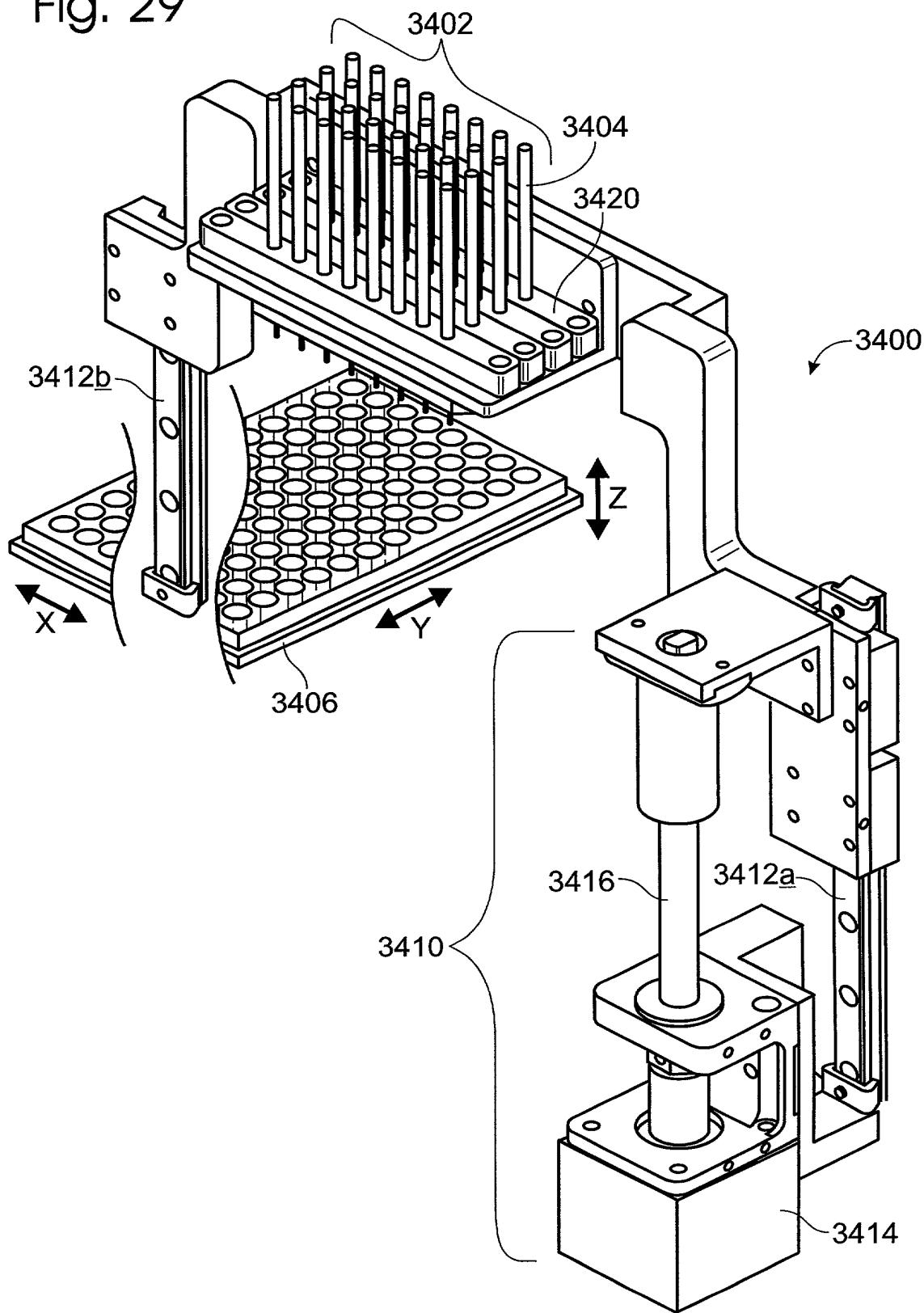


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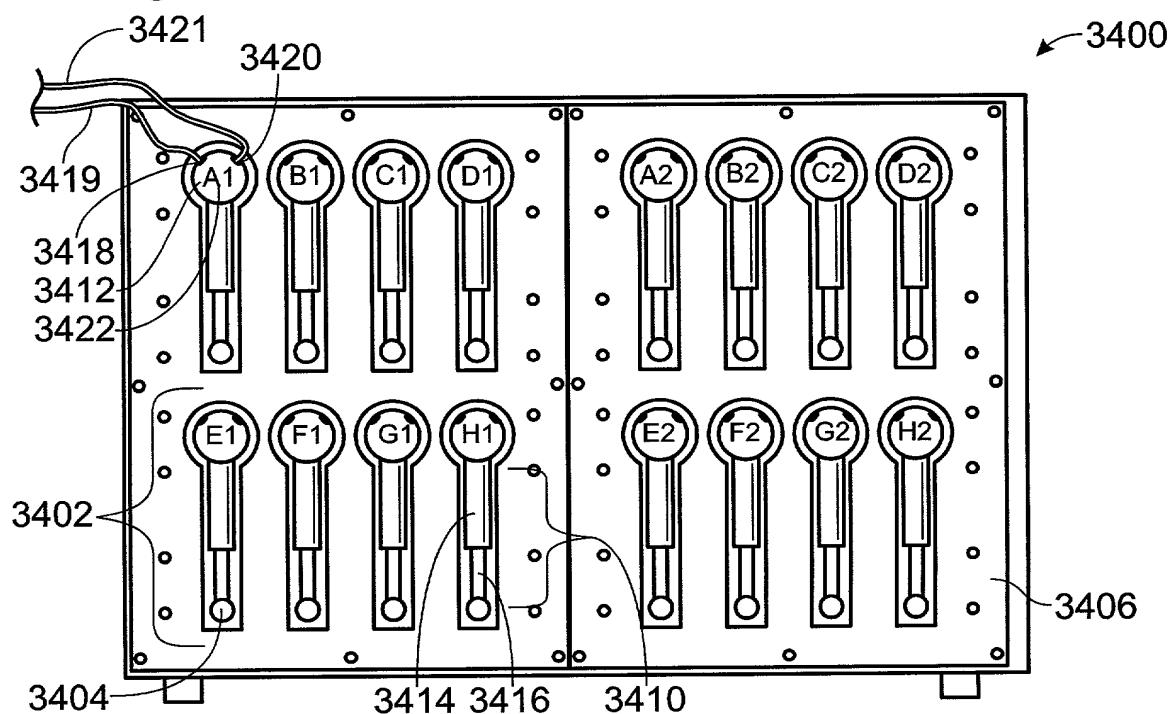


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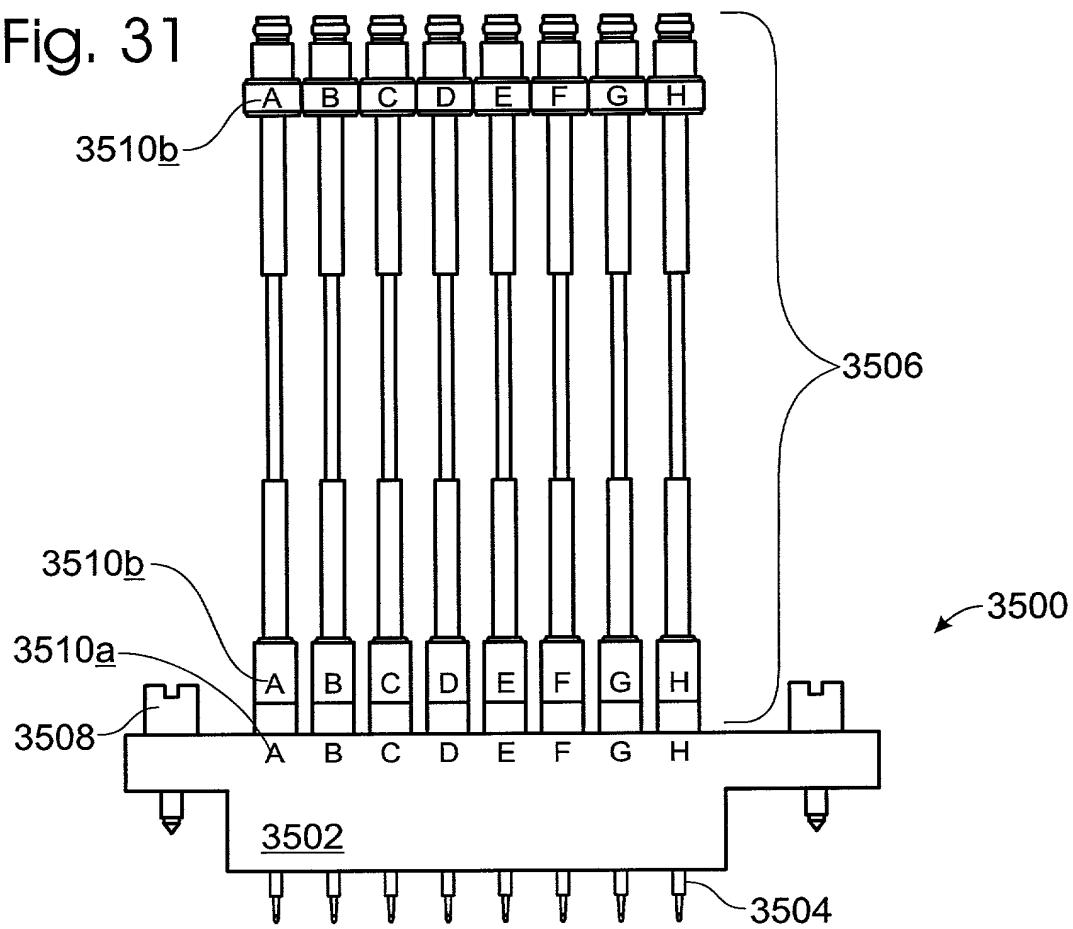


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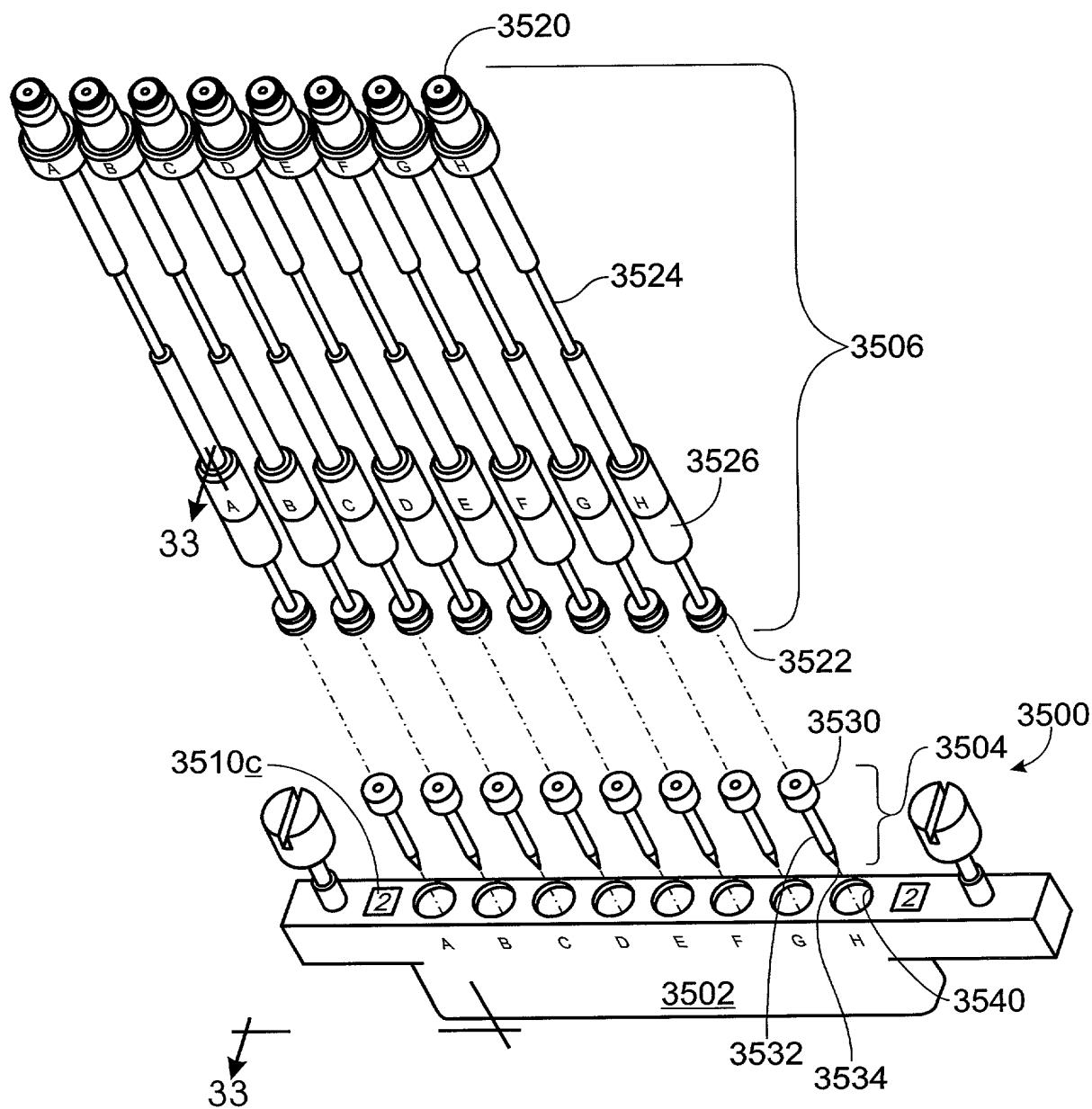


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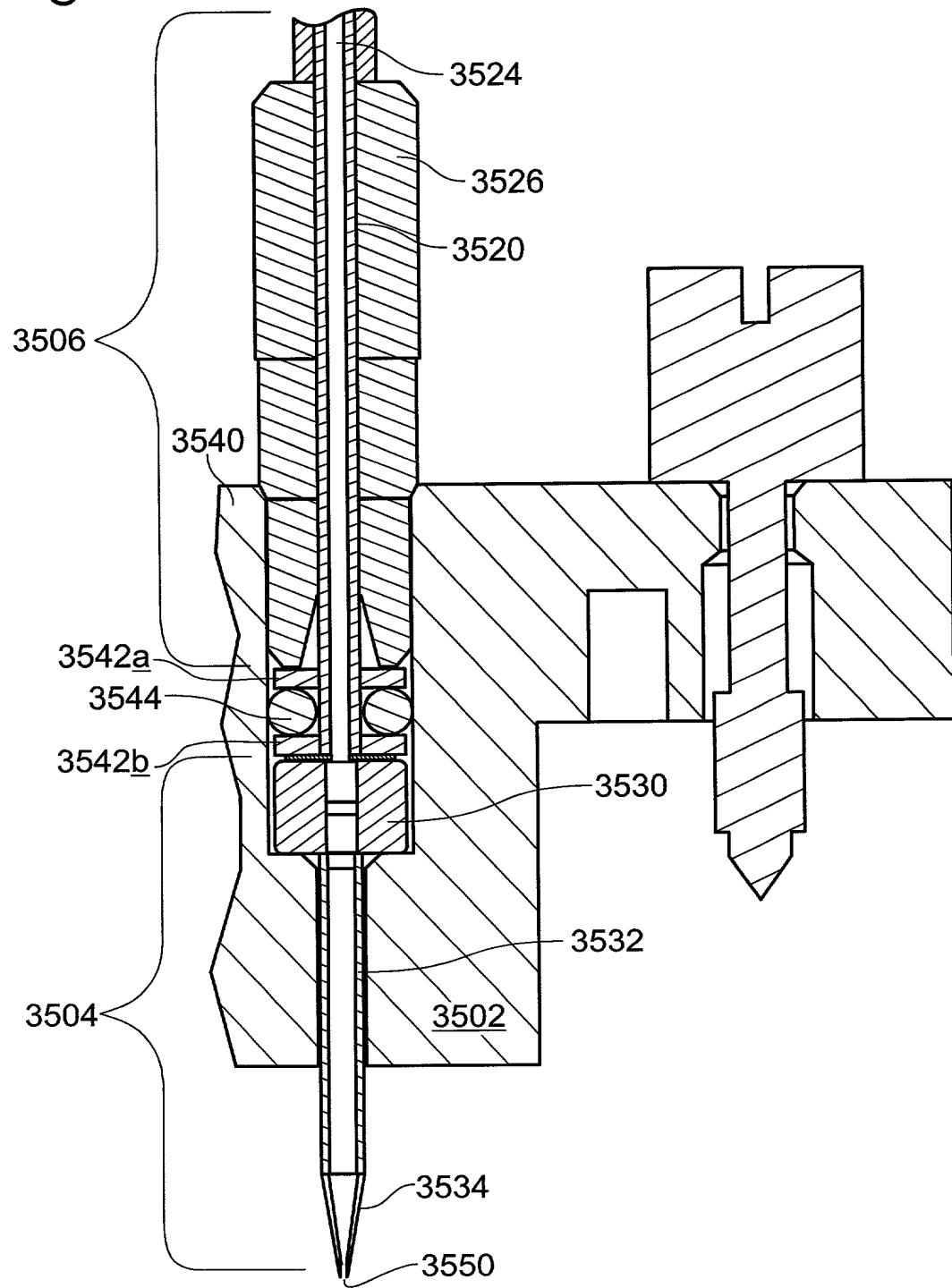


Fig. 34

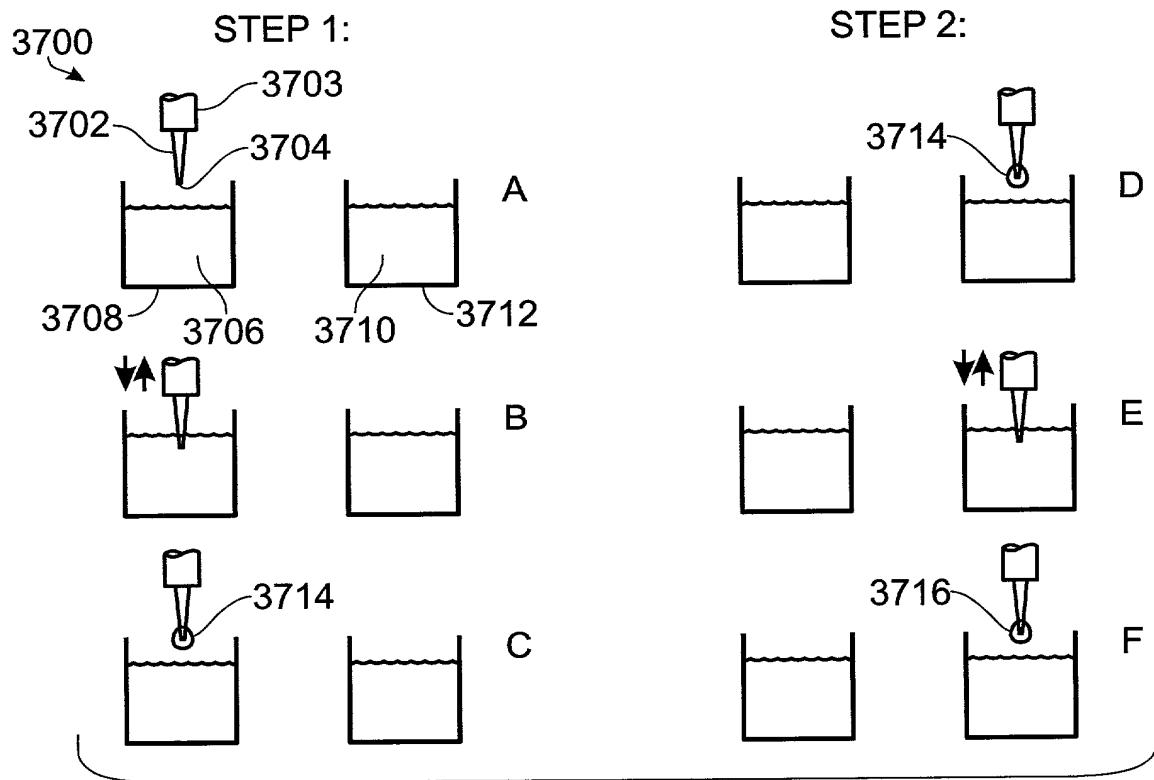


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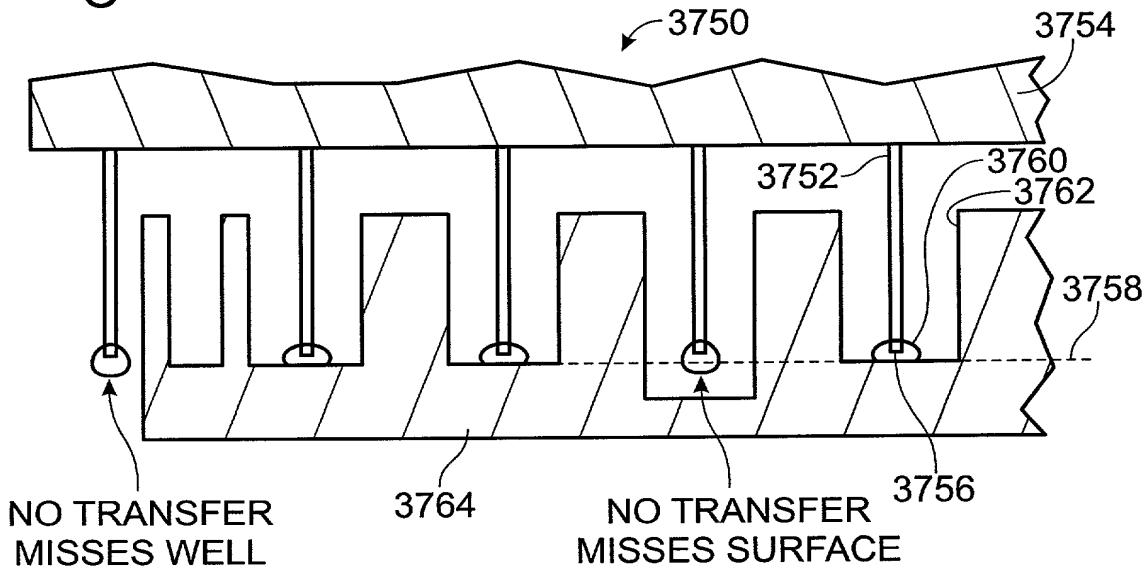


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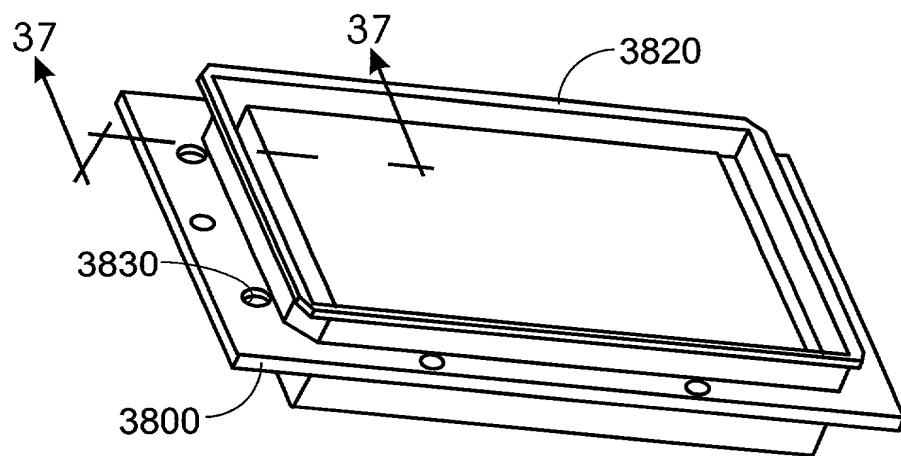


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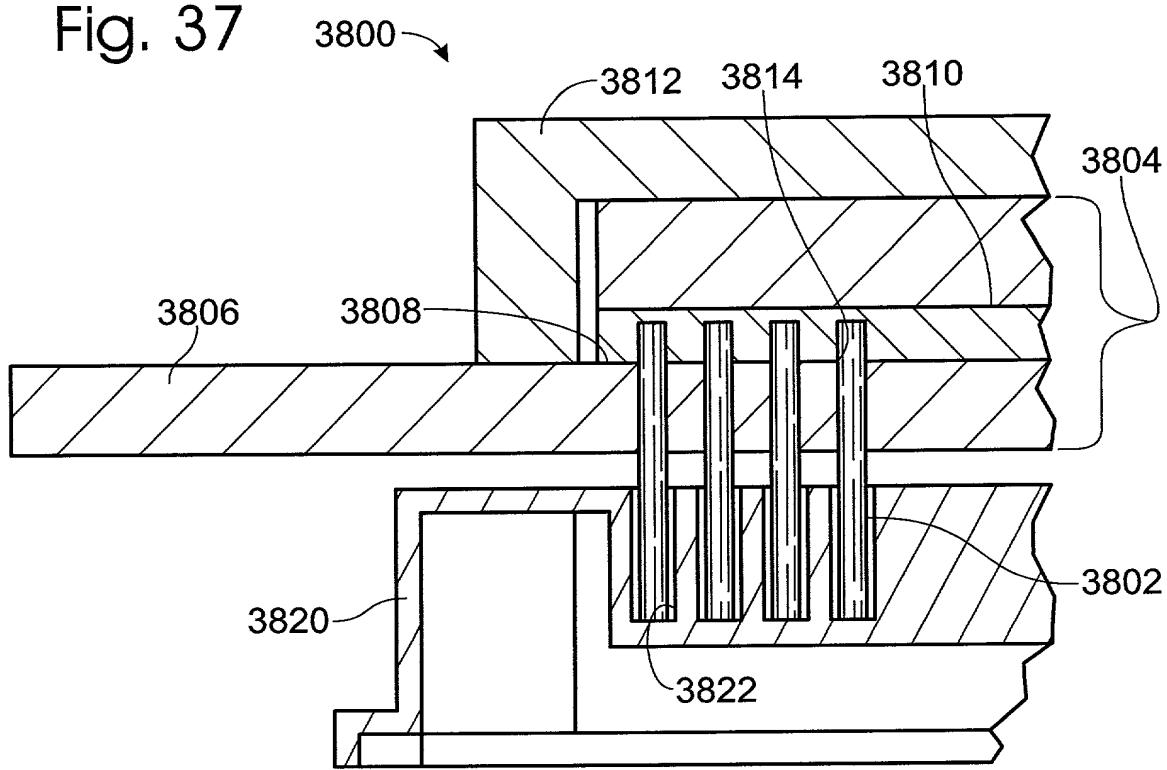


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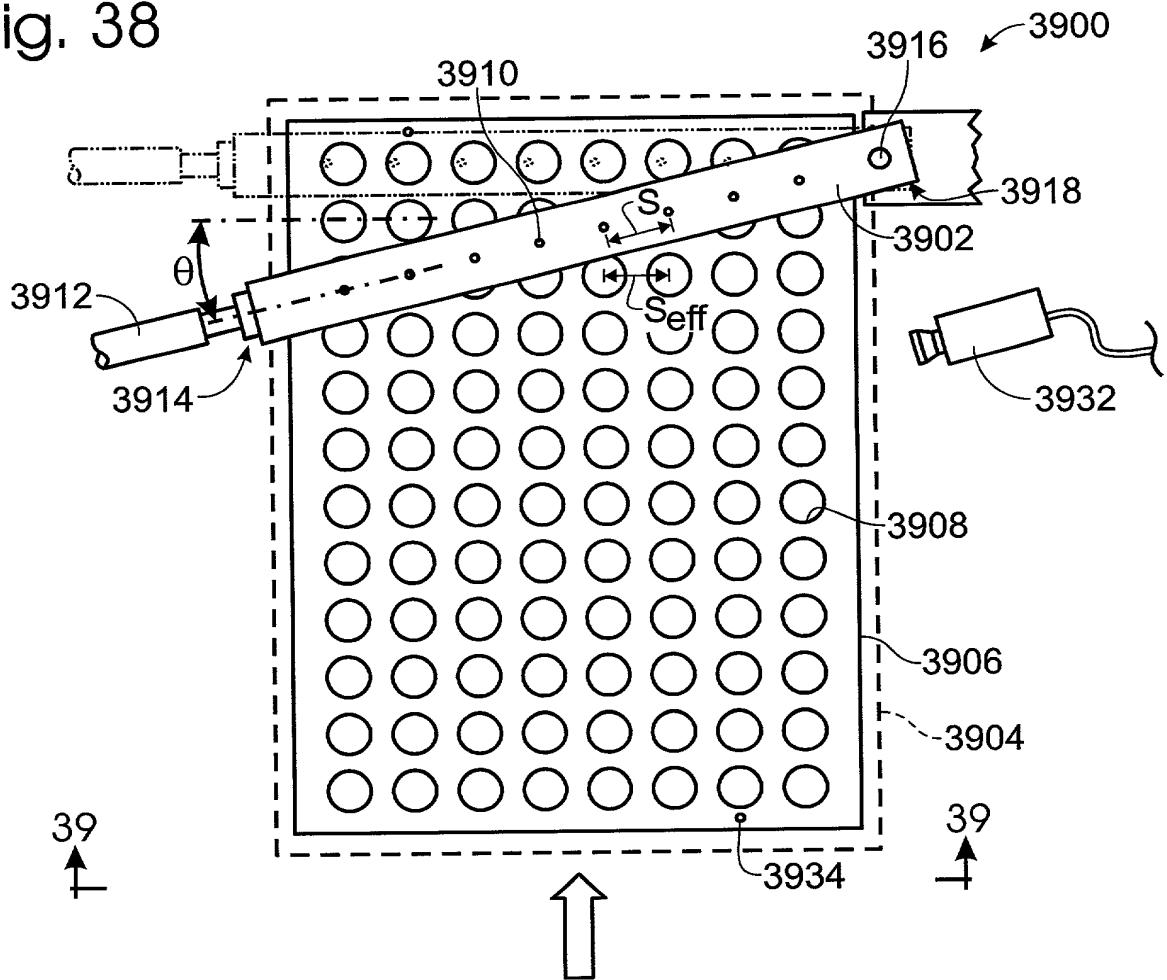


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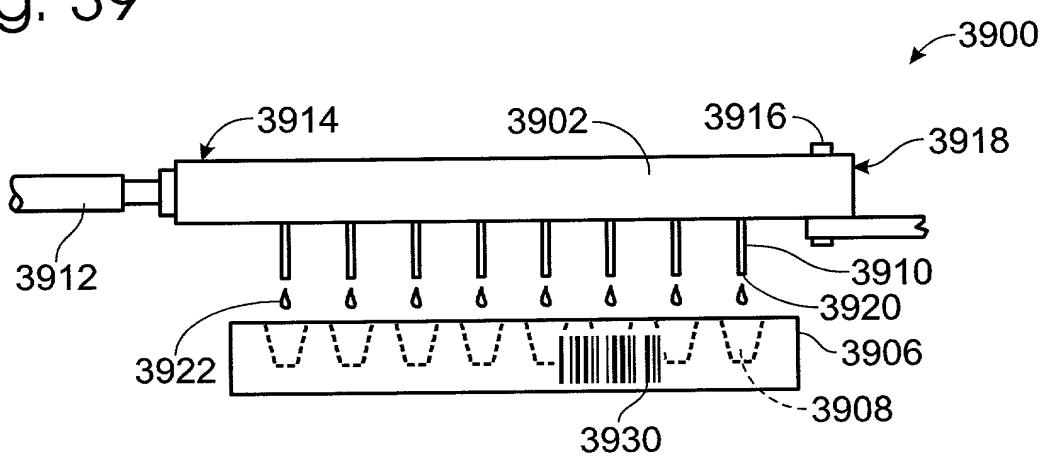


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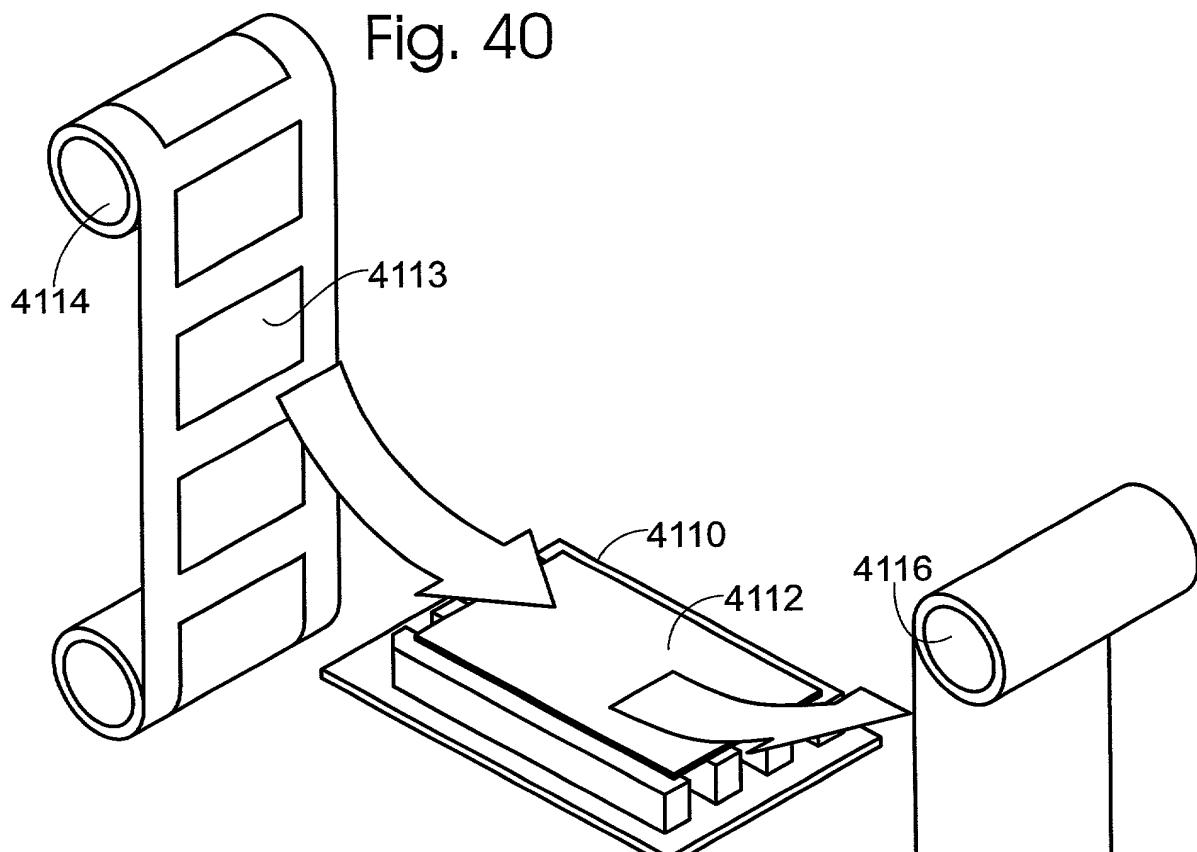


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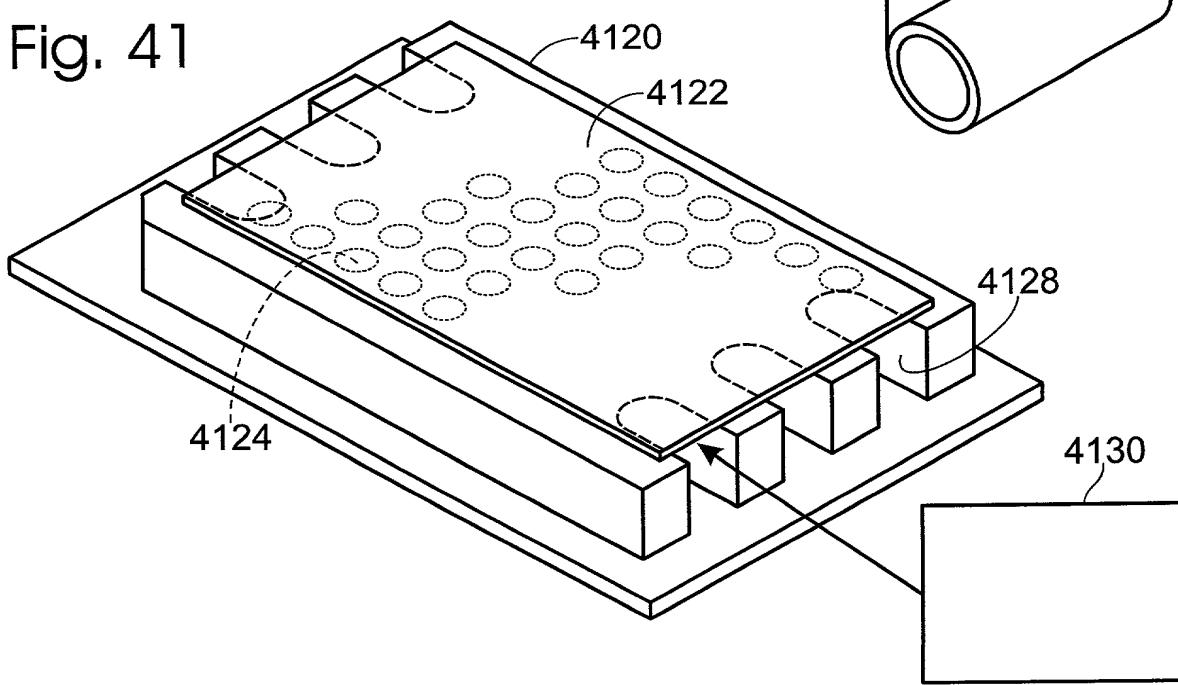


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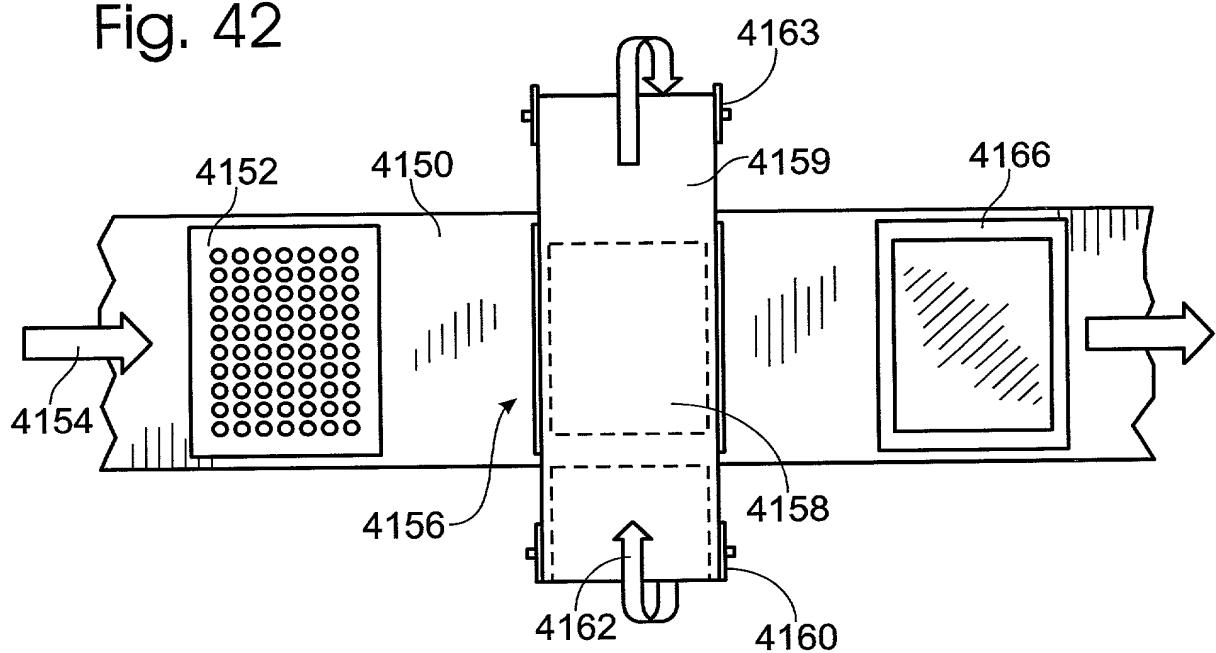


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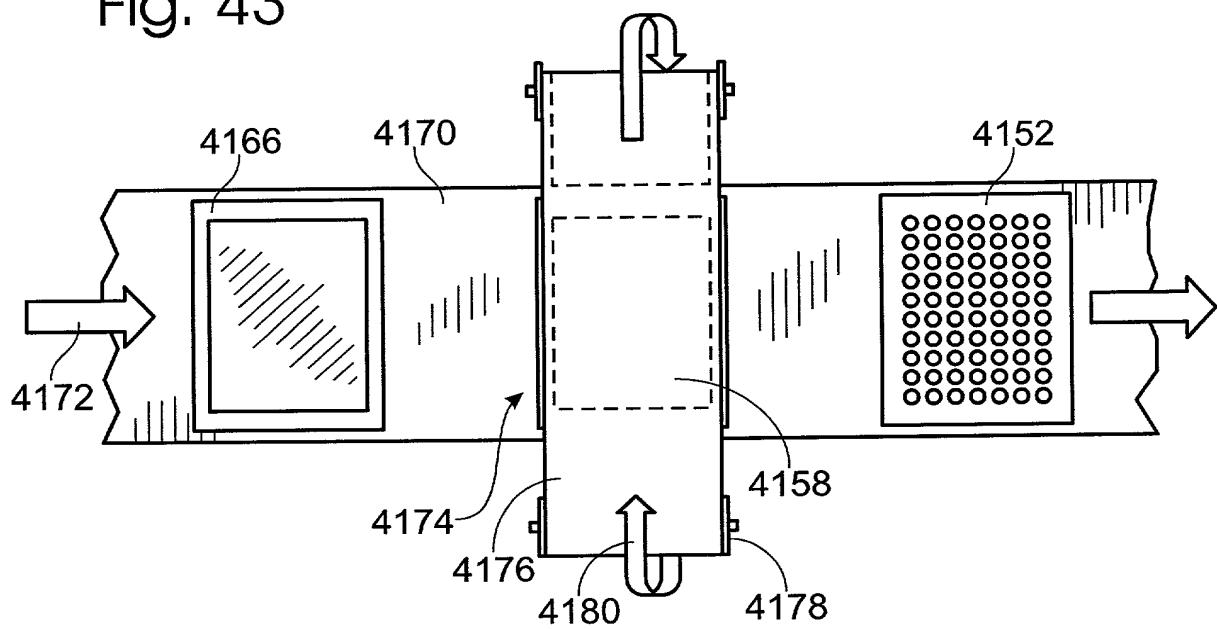


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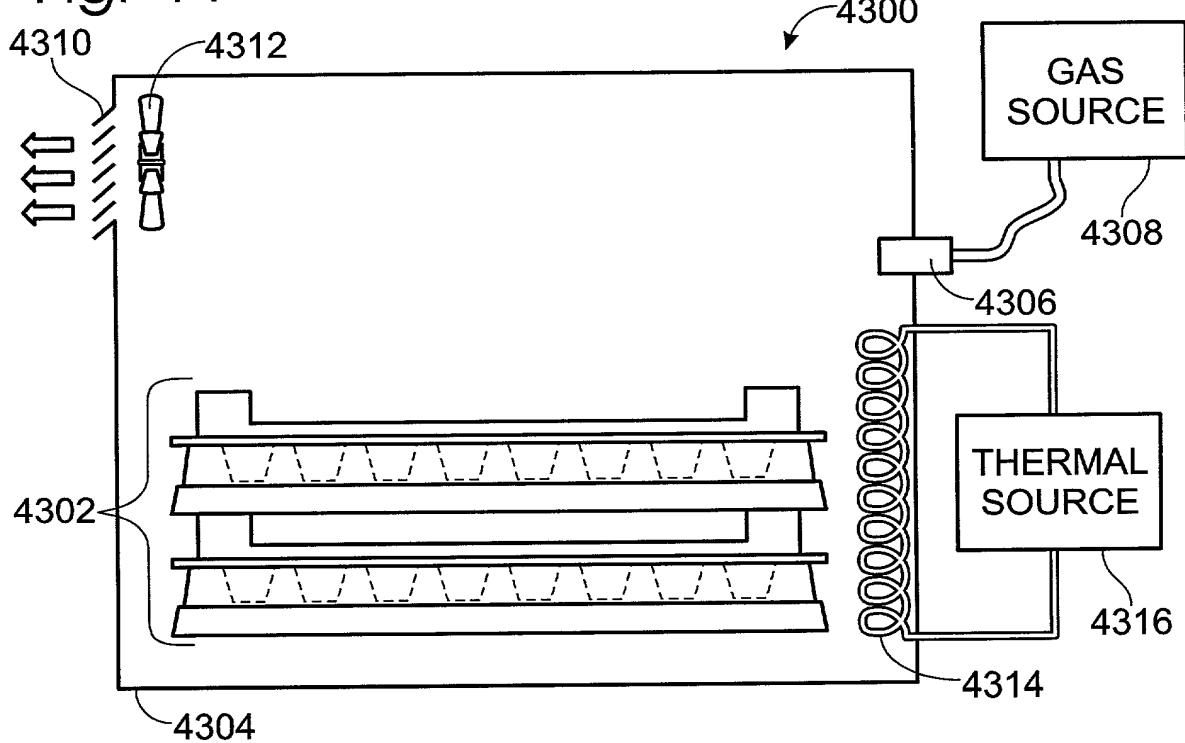


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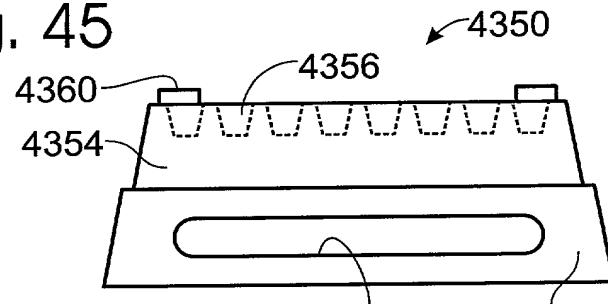


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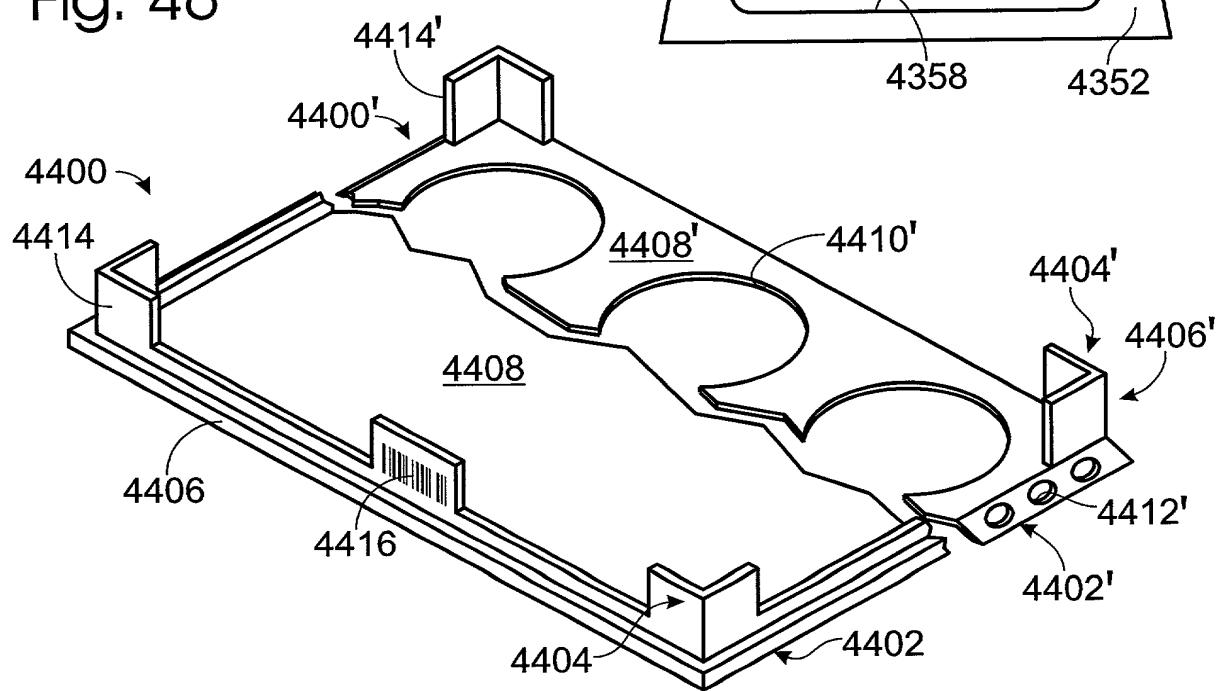


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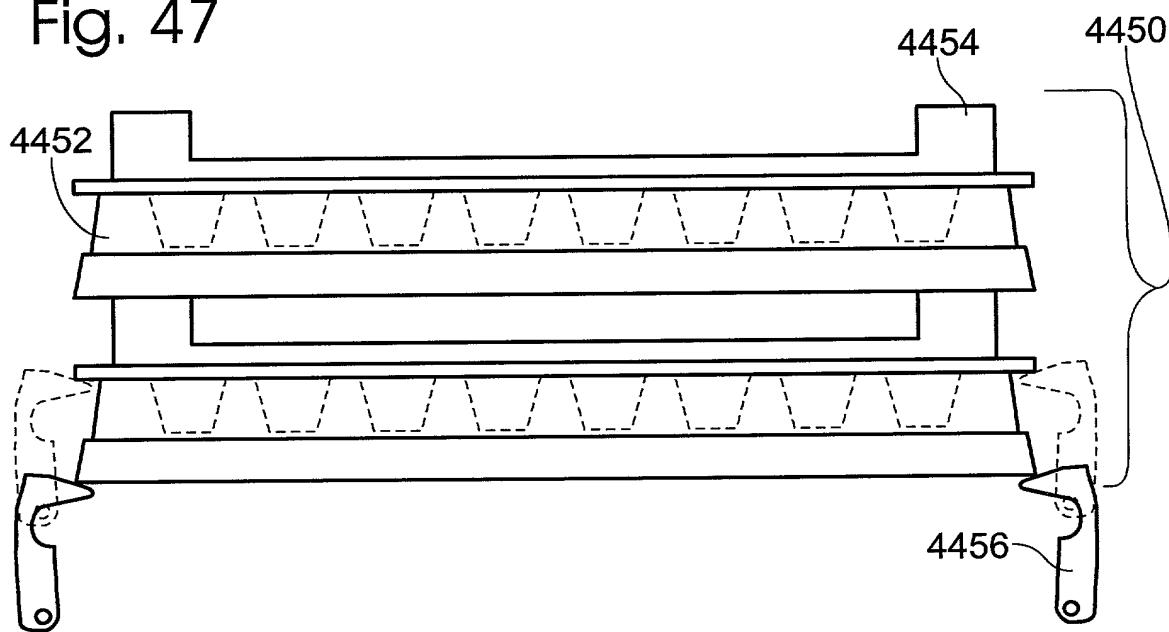
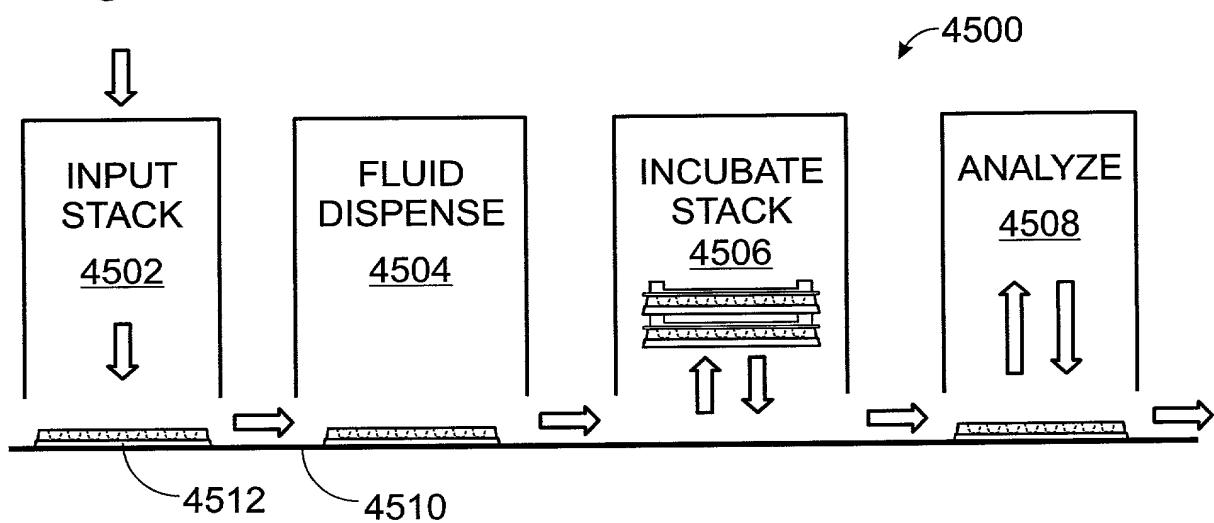


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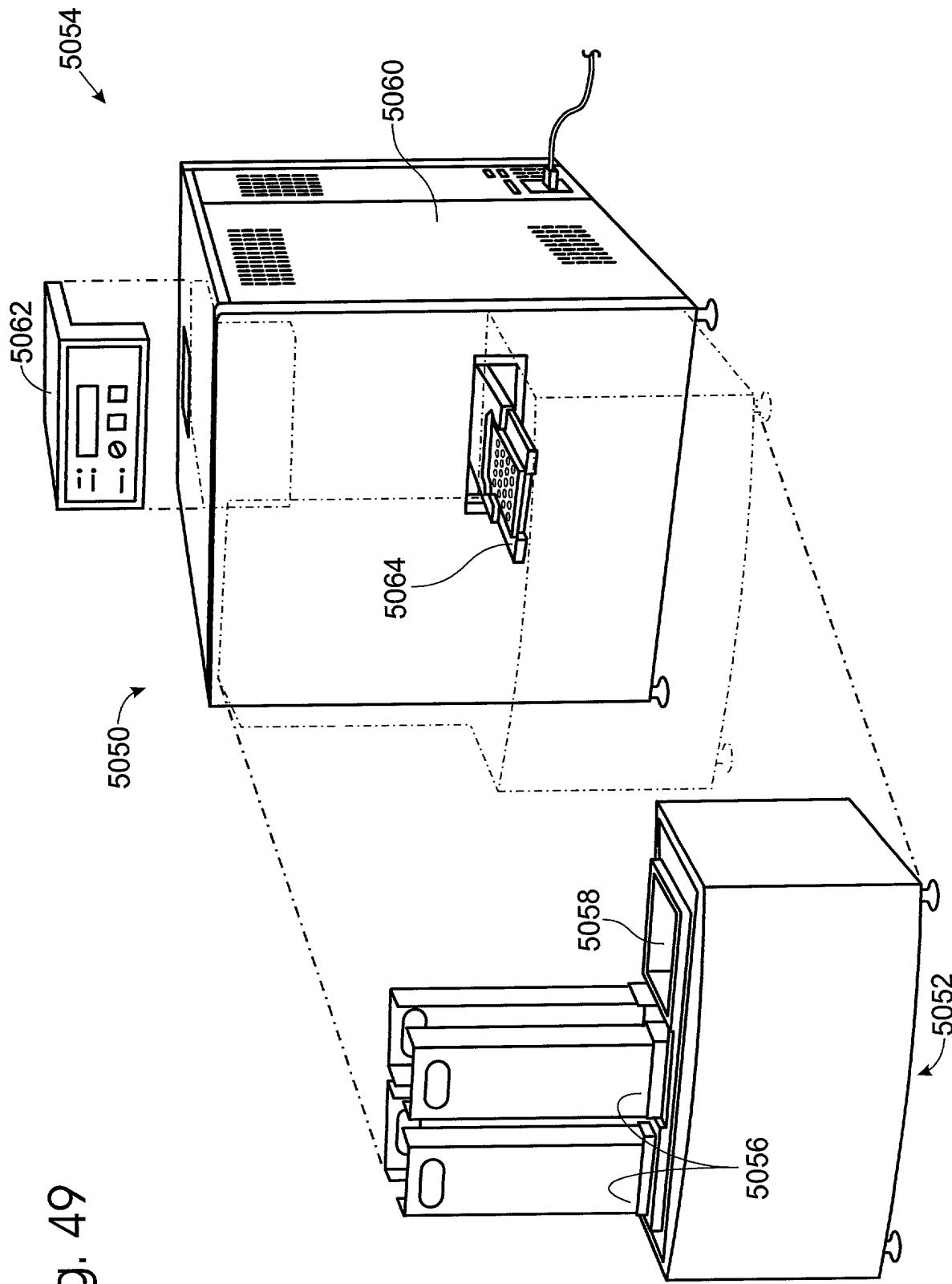


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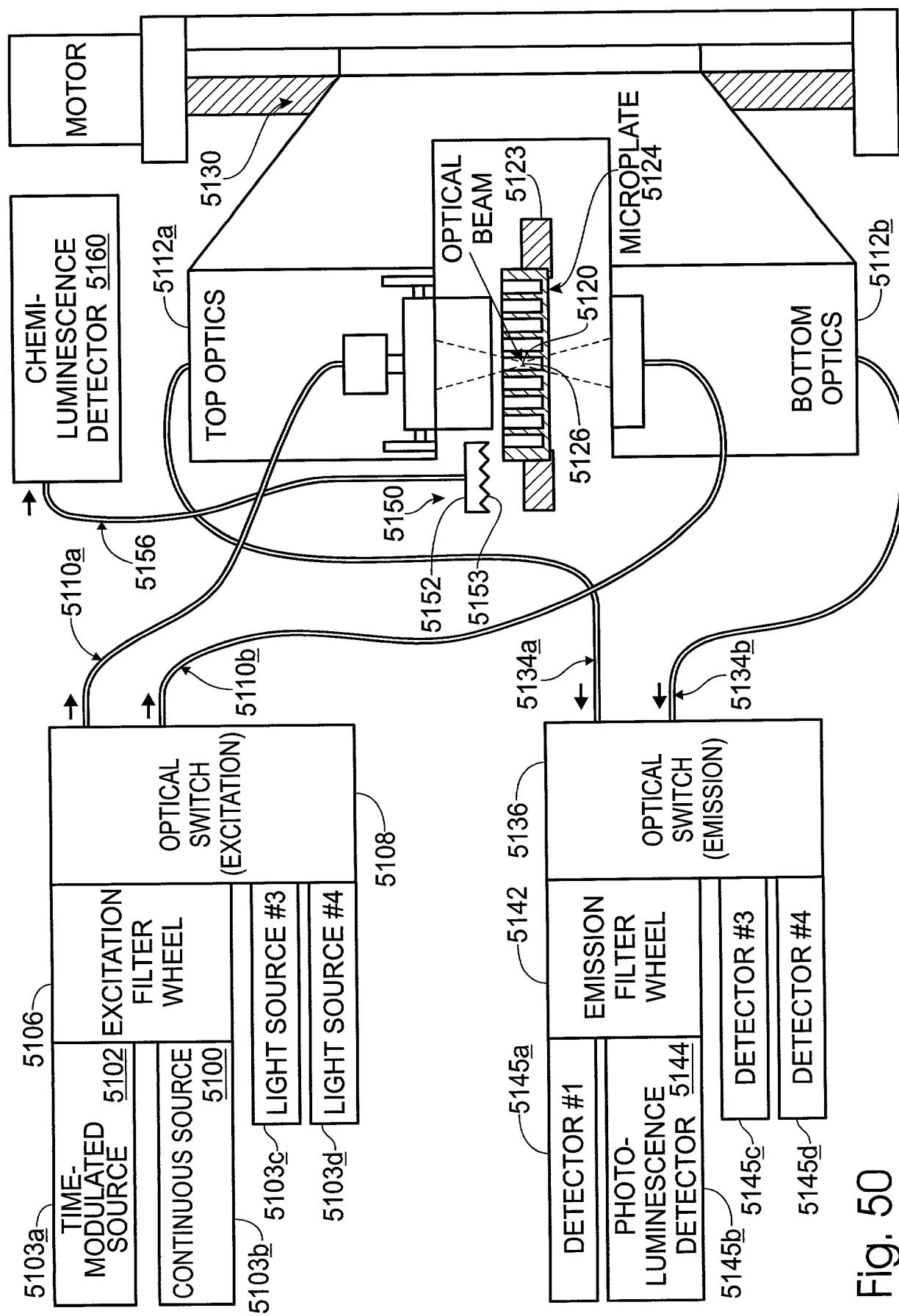


Fig. 50

Fig. 51

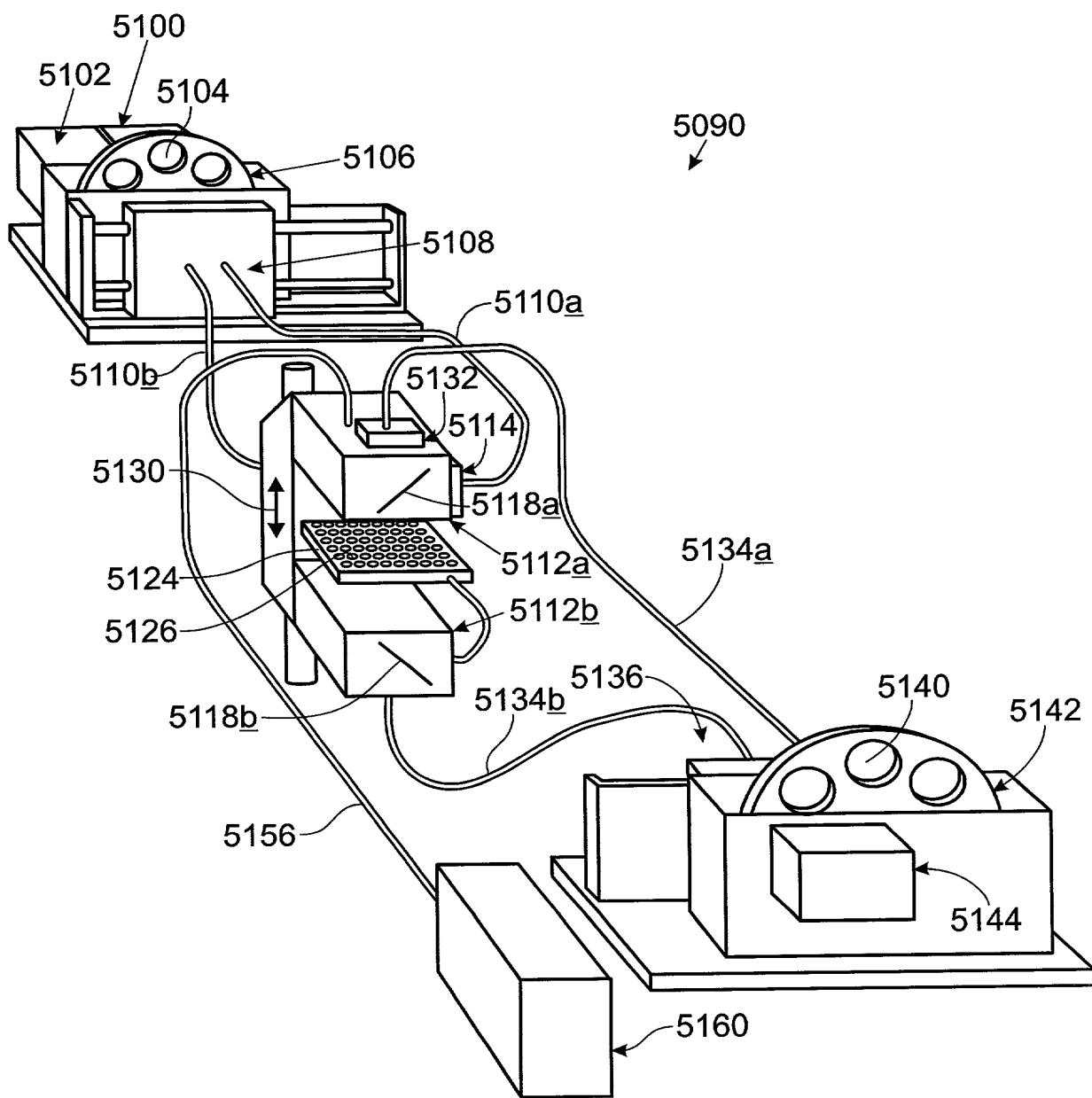


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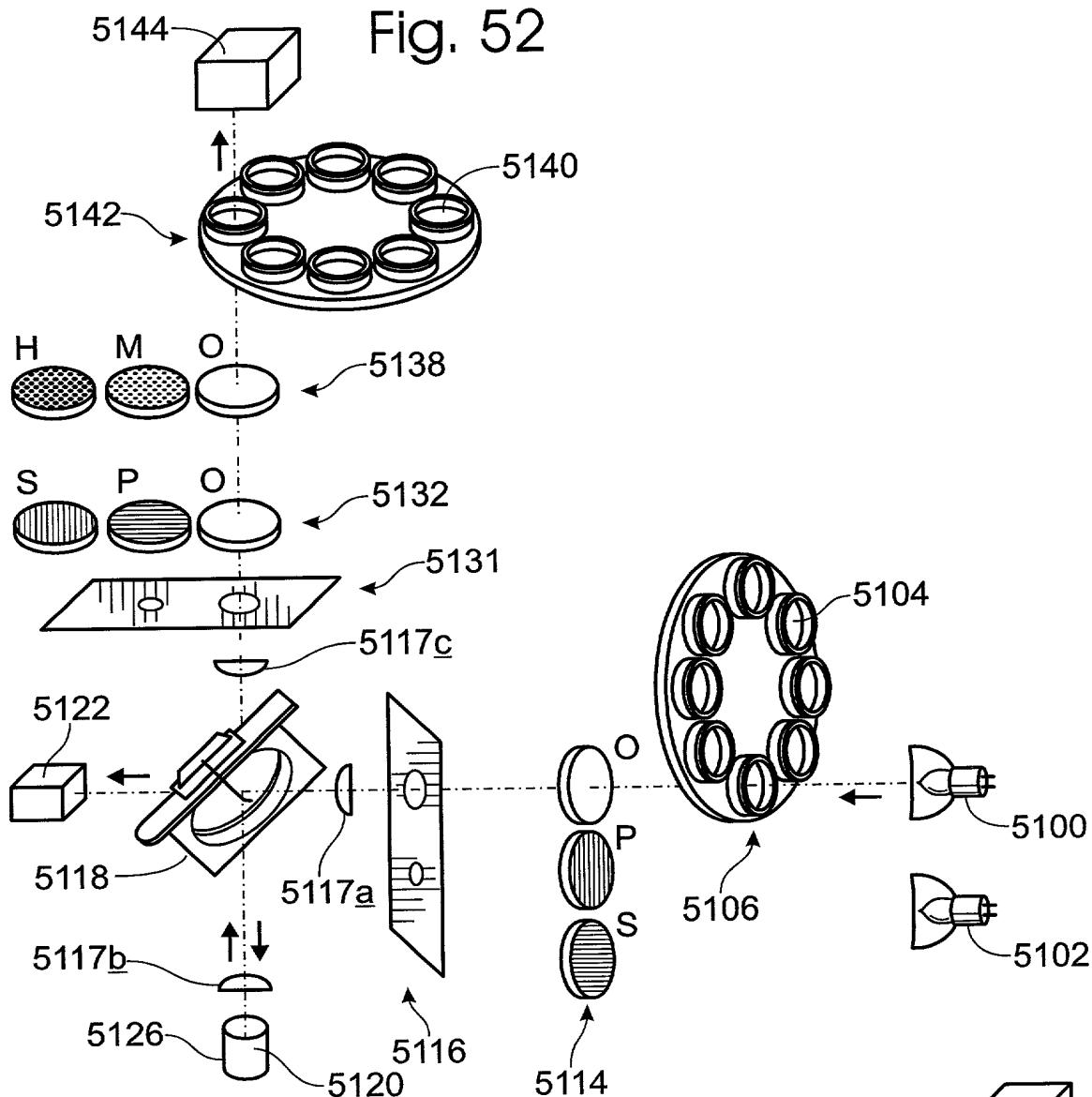


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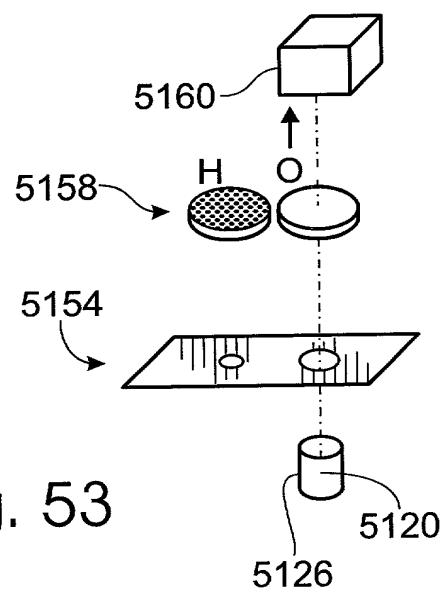


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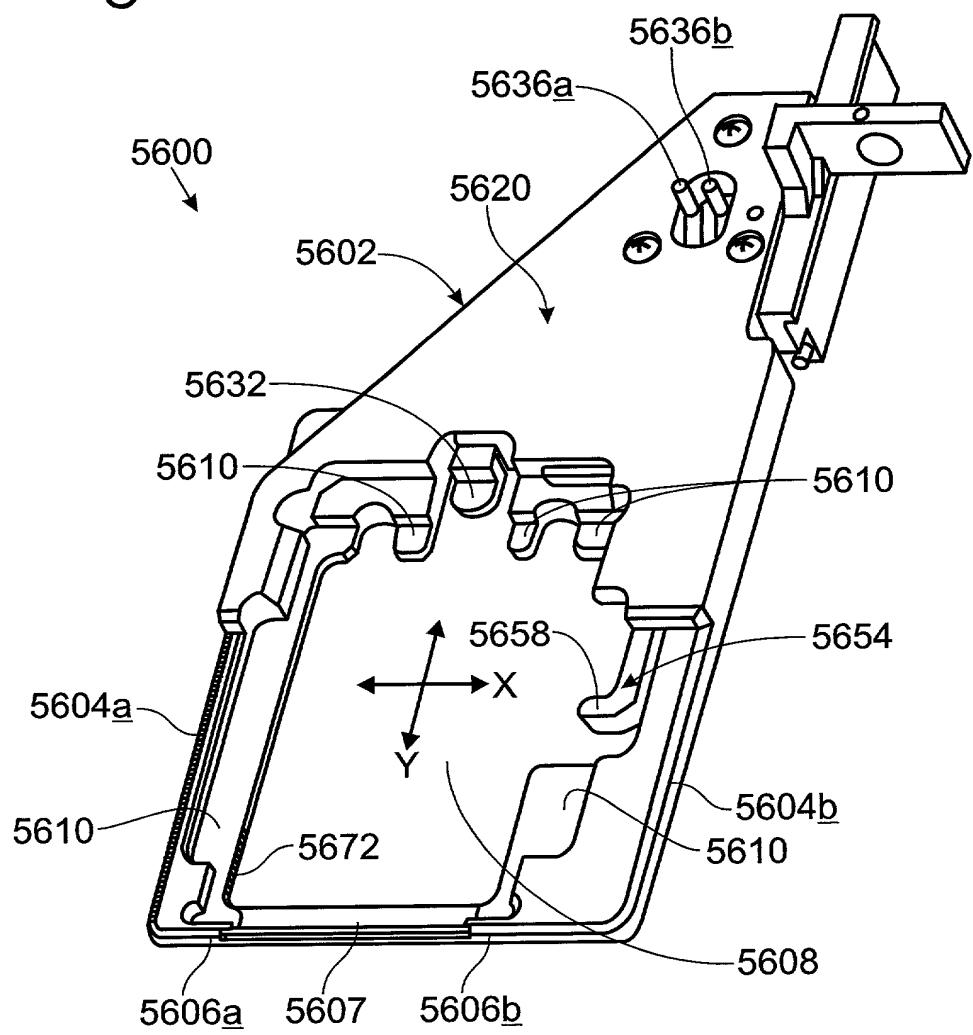


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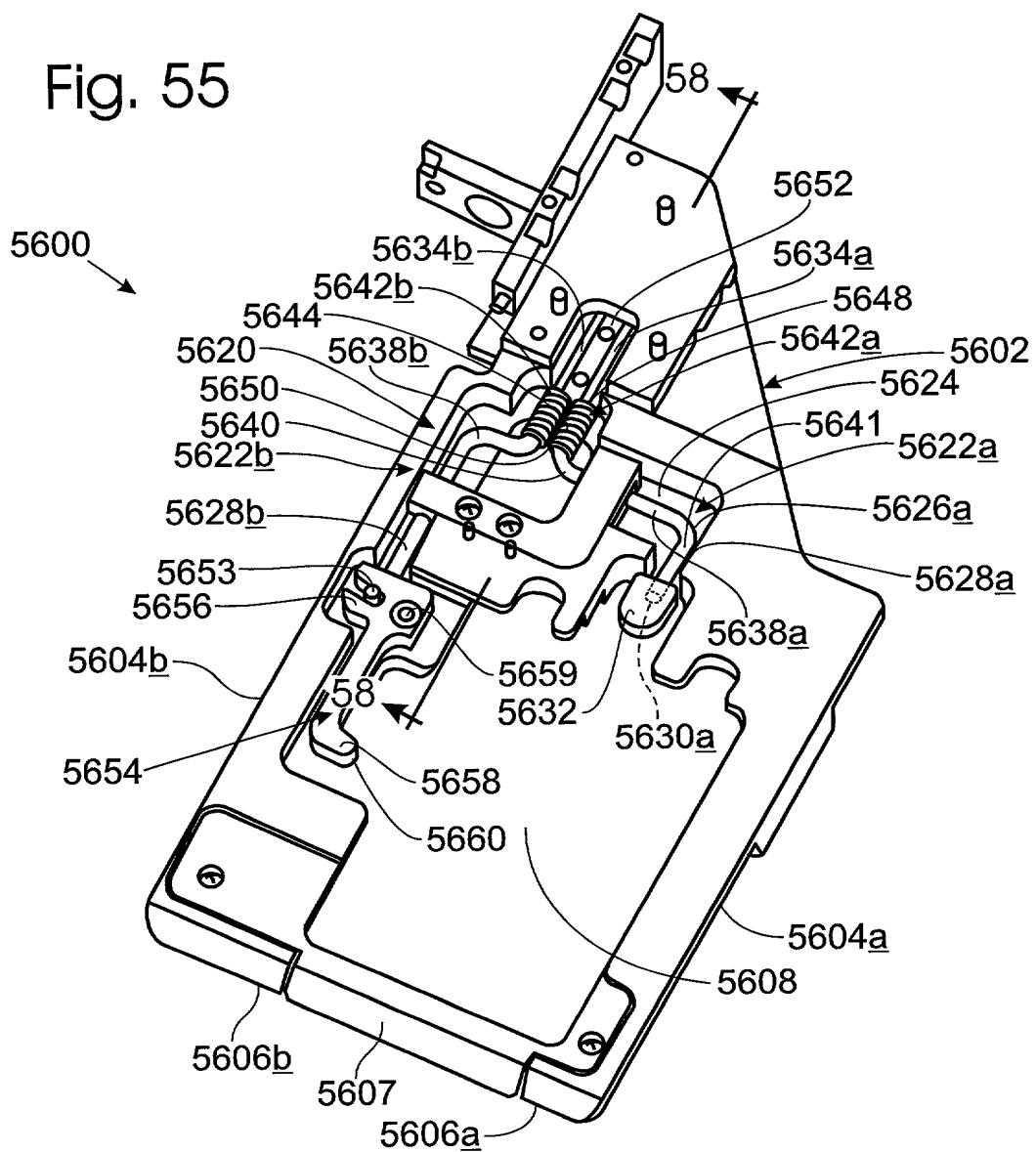


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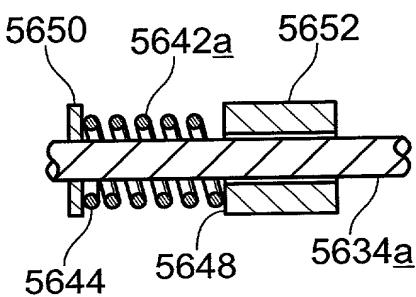


Fig. 57

